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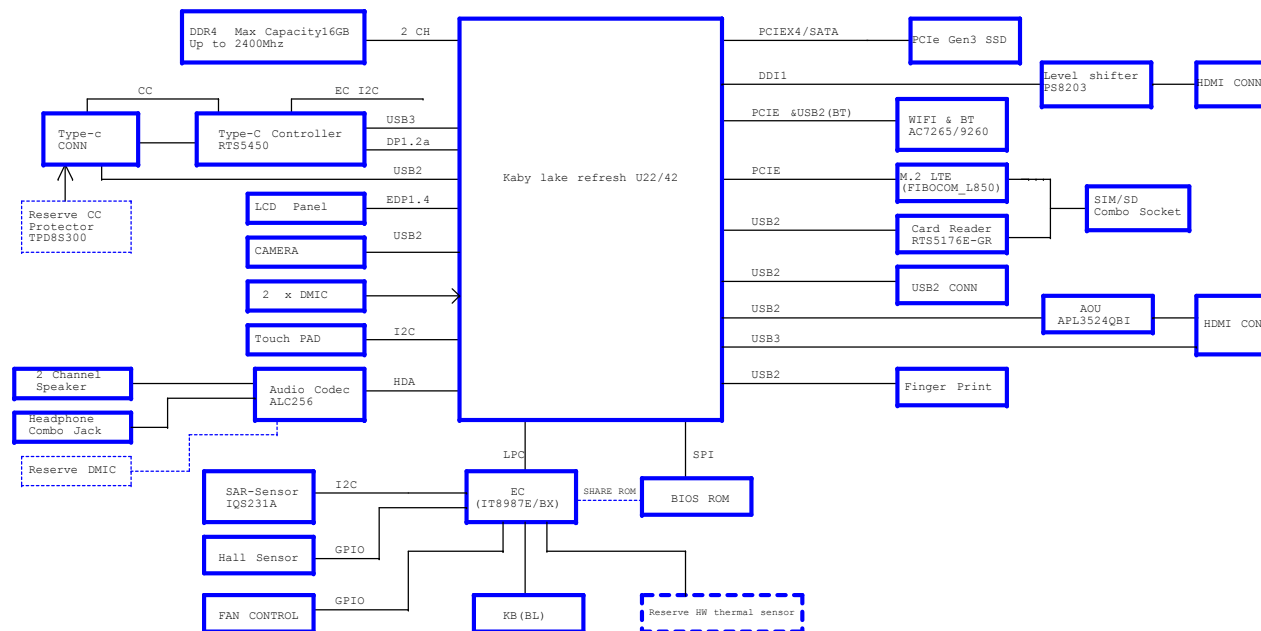
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21	DDR4 CH A (DEVICE0)	1.0		55	BLANK	1.0	
22	DDR4 CH A (DEVICE1)	1.0		56	DC IN & BATTERY CHARGER	1.0	
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25	DDR4 (DECAPS)	1.0		59	POWER DELIVERY 1.8V	1.0	
26	DDR4 CHA TERMINATIONS	1.0		60	POWER DELIVERY 1.05V	1.0	
27	DDR4 CHB TERMINATIONS	1.0		61	POWER DELIVERY-IMVP1	1.0	
28	SYSTEM FLASH	1.0		62	POWER DELIVERY 1.2V & 2.5V	1.0	
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30	SIM	1.0		64	POWER DELIVERY Reserve2	1.0	
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Pad Block Diagram



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B

A

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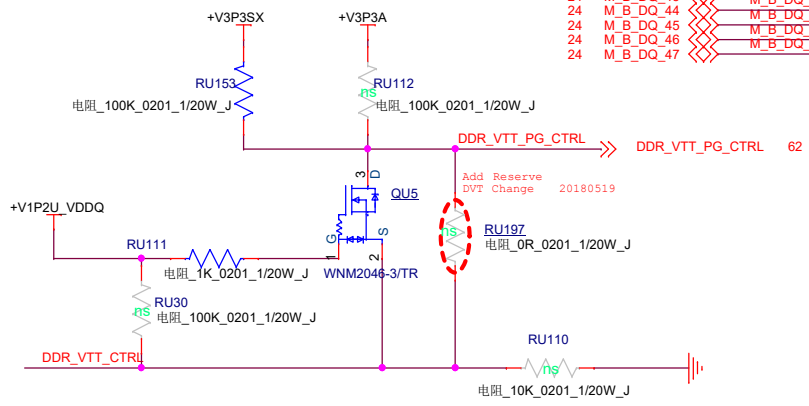
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Pin	Signal	Component	Value
21	M_A_DQ_0	AL71	DDR0_DQ[0]/DDR0_DQ[0]
21	M_A_DQ_1	AL68	DDR0_DQ[1]/DDR0_DQ[1]
21	M_A_DQ_2	AL68	DDR0_DQ[2]/DDR0_DQ[2]
21	M_A_DQ_3	AL68	DDR0_DQ[3]/DDR0_DQ[3]
21	M_A_DQ_4	AL70	DDR0_DQ[4]/DDR0_DQ[4]
21	M_A_DQ_5	AN70	DDR0_DQ[5]/DDR0_DQ[5]
21	M_A_DQ_6	AN71	DDR0_DQ[6]/DDR0_DQ[6]
21	M_A_DQ_7	AR70	DDR0_DQ[7]/DDR0_DQ[7]
21	M_A_DQ_8	AR68	DDR0_DQ[8]/DDR0_DQ[8]
21	M_A_DQ_9	AU71	DDR0_DQ[9]/DDR0_DQ[9]
21	M_A_DQ_10	AU68	DDR0_DQ[10]/DDR0_DQ[10]
21	M_A_DQ_11	AR71	DDR0_DQ[11]/DDR0_DQ[11]
21	M_A_DQ_12	AR69	DDR0_DQ[12]/DDR0_DQ[12]
21	M_A_DQ_13	AU70	DDR0_DQ[13]/DDR0_DQ[13]
21	M_A_DQ_14	AU69	DDR0_DQ[14]/DDR0_DQ[14]
21	M_A_DQ_15	BB65	DDR0_DQ[15]/DDR0_DQ[15]
22	M_A_DQ_32	AW65	DDR0_DQ[16]/DDR0_DQ[32]
22	M_A_DQ_33	AW65	DDR0_DQ[17]/DDR0_DQ[33]
22	M_A_DQ_34	AY63	DDR0_DQ[18]/DDR0_DQ[34]
22	M_A_DQ_35	BA65	DDR0_DQ[19]/DDR0_DQ[35]
22	M_A_DQ_36	AY65	DDR0_DQ[20]/DDR0_DQ[36]
22	M_A_DQ_37	BA63	DDR0_DQ[21]/DDR0_DQ[37]
22	M_A_DQ_38	BB63	DDR0_DQ[22]/DDR0_DQ[38]
22	M_A_DQ_39	BA61	DDR0_DQ[23]/DDR0_DQ[39]
22	M_A_DQ_40	AW61	DDR0_DQ[24]/DDR0_DQ[40]
22	M_A_DQ_41	BB59	DDR0_DQ[25]/DDR0_DQ[41]
22	M_A_DQ_42	AW59	DDR0_DQ[26]/DDR0_DQ[42]
22	M_A_DQ_43	BB61	DDR0_DQ[27]/DDR0_DQ[43]
22	M_A_DQ_44	AY61	DDR0_DQ[28]/DDR0_DQ[44]
22	M_A_DQ_45	BA59	DDR0_DQ[29]/DDR0_DQ[45]
22	M_A_DQ_46	AY59	DDR0_DQ[30]/DDR0_DQ[46]
22	M_A_DQ_47	AY39	DDR0_DQ[31]/DDR0_DQ[47]
23	M_B_DQ_0	AW39	DDR0_DQ[32]/DDR1_DQ[0]
23	M_B_DQ_1	AY37	DDR0_DQ[33]/DDR1_DQ[1]
23	M_B_DQ_2	AW37	DDR0_DQ[34]/DDR1_DQ[2]
23	M_B_DQ_3	BB39	DDR0_DQ[35]/DDR1_DQ[3]
23	M_B_DQ_4	BA39	DDR0_DQ[36]/DDR1_DQ[4]
23	M_B_DQ_5	BA37	DDR0_DQ[37]/DDR1_DQ[5]
23	M_B_DQ_6	BB37	DDR0_DQ[38]/DDR1_DQ[6]
23	M_B_DQ_7	AY35	DDR0_DQ[39]/DDR1_DQ[7]
23	M_B_DQ_8	AY35	DDR0_DQ[40]/DDR1_DQ[8]
23	M_B_DQ_9	AY35	DDR0_DQ[41]/DDR1_DQ[9]
23	M_B_DQ_10	AY33	DDR0_DQ[42]/DDR1_DQ[10]
23	M_B_DQ_11	BB35	DDR0_DQ[43]/DDR1_DQ[11]
23	M_B_DQ_12	BA35	DDR0_DQ[44]/DDR1_DQ[12]
23	M_B_DQ_13	BA33	DDR0_DQ[45]/DDR1_DQ[13]
23	M_B_DQ_14	BB33	DDR0_DQ[46]/DDR1_DQ[14]
23	M_B_DQ_15	AY31	DDR0_DQ[47]/DDR1_DQ[15]
23	M_B_DQ_32	AY31	DDR0_DQ[48]/DDR1_DQ[32]
23	M_B_DQ_33	AY29	DDR0_DQ[49]/DDR1_DQ[33]
23	M_B_DQ_34	AY29	DDR0_DQ[50]/DDR1_DQ[34]
23	M_B_DQ_35	AW29	DDR0_DQ[51]/DDR1_DQ[35]
23	M_B_DQ_36	BB31	DDR0_DQ[52]/DDR1_DQ[36]
23	M_B_DQ_37	BA29	DDR0_DQ[53]/DDR1_DQ[37]
23	M_B_DQ_38	BB29	DDR0_DQ[54]/DDR1_DQ[38]
23	M_B_DQ_39	AY27	DDR0_DQ[55]/DDR1_DQ[39]
24	M_B_DQ_40	AW27	DDR0_DQ[56]/DDR1_DQ[40]
24	M_B_DQ_41	AY25	DDR0_DQ[57]/DDR1_DQ[41]
24	M_B_DQ_42	AW25	DDR0_DQ[58]/DDR1_DQ[42]
24	M_B_DQ_43	BB27	DDR0_DQ[59]/DDR1_DQ[43]
24	M_B_DQ_44	BA27	DDR0_DQ[60]/DDR1_DQ[44]
24	M_B_DQ_45	BA25	DDR0_DQ[61]/DDR1_DQ[45]
24	M_B_DQ_46	BA25	DDR0_DQ[62]/DDR1_DQ[46]
24	M_B_DQ_47	BB25	DDR0_DQ[63]/DDR1_DQ[47]

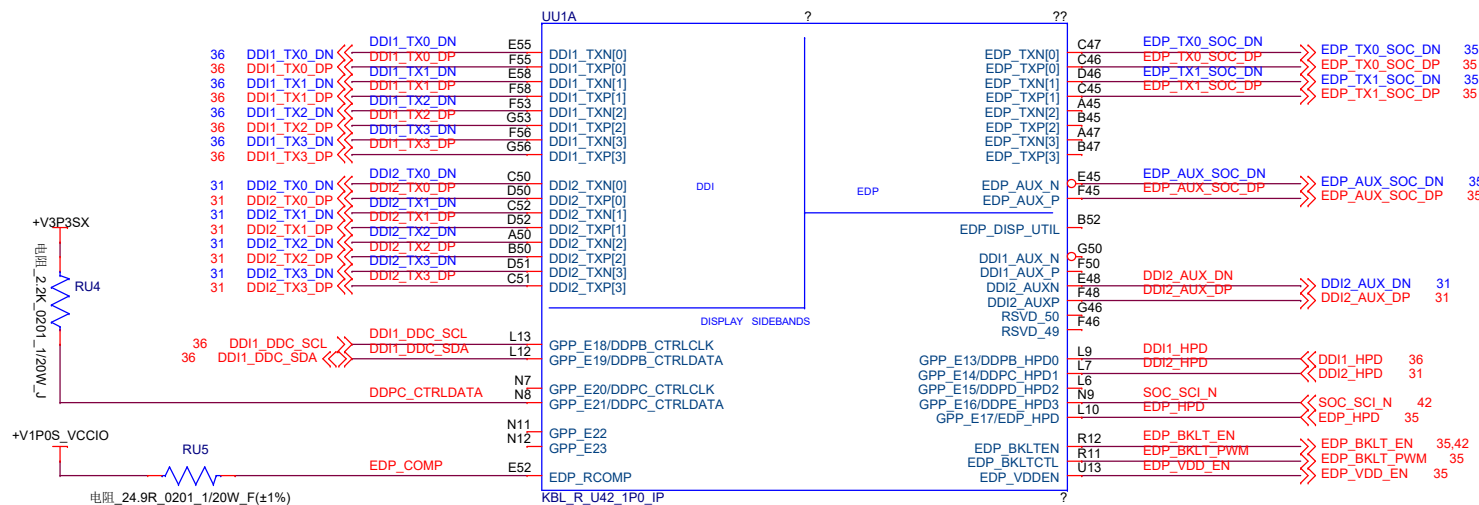
Pin	Signal	Component	Value
425	DDR4(ILVLP3-DDR4) (NIL)		
425	DDR0_CK[0]/DDR0_CK[0]	AU53	M_A_CK_DDR0_DN
425	DDR0_CK[0]/DDR0_CK[0]	AT53	M_A_CK_DDR0_DP
425	DDR0_CK[1]/DDR0_CK[1]	AU55	
425	DDR0_CK[1]/DDR0_CK[1]	AT55	
425	DDR0_CK[1]/DDR0_CK[1]	BA56	M_A_CKE0
425	DDR0_CK[1]/DDR0_CK[1]	BB56	
425	DDR0_CK[1]/DDR0_CK[1]	AW56	
425	DDR0_CK[1]/DDR0_CK[1]	AY56	
425	DDR0_CS#(0)/DDR0_CS#(0)	AU45	M_A_CS0_N
425	DDR0_CS#(1)/DDR0_CS#(1)	AU43	
425	DDR0_ODT(0)/DDR0_ODT(0)	AT43	M_A_ODT0
425	DDR0_ODT(1)/DDR0_ODT(1)		
425	DDR0_CAA[0]/DDR0_CAA[0]	BA51	M_A_MA_5
425	DDR0_CAA[0]/DDR0_CAA[0]	BB54	M_A_MA_9
425	DDR0_CAA[0]/DDR0_CAA[0]	BA52	M_A_MA_6
425	DDR0_CAA[0]/DDR0_CAA[0]	AY52	M_A_MA_8
425	DDR0_CAA[0]/DDR0_CAA[0]	AW52	M_A_MA_7
425	DDR0_CAA[0]/DDR0_CAA[0]	AY55	M_A_BG0
425	DDR0_CAA[0]/DDR0_CAA[0]	AW54	M_A_MA_12
425	DDR0_CAA[0]/DDR0_CAA[0]	BA54	M_A_MA_11
425	DDR0_CAA[0]/DDR0_CAA[0]	BA55	M_A_ACT_N
425	DDR0_CAA[0]/DDR0_CAA[0]	AY54	M_A_BG1
425	DDR0_CAA[0]/DDR0_CAA[0]		M_A_BG1 21
425	DDR0_CAB[0]/DDR0_CAB[0]	AU46	M_A_MA_13
425	DDR0_CAB[0]/DDR0_CAB[0]	AU48	M_A_MA_15
425	DDR0_CAB[0]/DDR0_CAB[0]	AT46	M_A_MA_14
425	DDR0_CAB[0]/DDR0_CAB[0]	AU50	M_A_MA_16
425	DDR0_CAB[0]/DDR0_CAB[0]	AU52	M_A_MA_16
425	DDR0_CAB[0]/DDR0_CAB[0]	AU50	M_A_BA0
425	DDR0_CAB[0]/DDR0_CAB[0]	AT48	M_A_BA1
425	DDR0_CAB[0]/DDR0_CAB[0]	AT50	M_A_MA_10
425	DDR0_CAB[0]/DDR0_CAB[0]	BB50	M_A_MA_10
425	DDR0_CAB[0]/DDR0_CAB[0]	AY50	M_A_MA_1
425	DDR0_CAB[0]/DDR0_CAB[0]	BA50	M_A_MA_3
425	DDR0_CAB[0]/DDR0_CAB[0]	BB52	M_A_MA_4
425	DDR0_CAB[0]/DDR0_CAB[0]		M_A_MA_4 21,22,26
425	DDR0_DQSN(0)/DDR0_DQSN(0)	AM70	M_A_DQS_0_DN
425	DDR0_DQSN(0)/DDR0_DQSN(0)	AM69	M_A_DQS_0_DP
425	DDR0_DQSN(0)/DDR0_DQSN(0)	AT69	M_A_DQS_1_DN
425	DDR0_DQSN(0)/DDR0_DQSN(0)	AT70	M_A_DQS_1_DP
425	DDR0_DQSN(0)/DDR0_DQSN(0)	BA64	M_A_DQS_4_DN
425	DDR0_DQSN(0)/DDR0_DQSN(0)	AY64	M_A_DQS_4_DP
425	DDR0_DQSN(0)/DDR0_DQSN(0)	AY60	M_A_DQS_5_DN
425	DDR0_DQSN(0)/DDR0_DQSN(0)	BA60	M_A_DQS_5_DP
425	DDR0_DQSN(0)/DDR0_DQSN(0)	BA38	M_B_DQS_0_DN
425	DDR0_DQSN(0)/DDR0_DQSN(0)	AY38	M_B_DQS_0_DP
425	DDR0_DQSN(0)/DDR0_DQSN(0)	AY34	M_B_DQS_1_DN
425	DDR0_DQSN(0)/DDR0_DQSN(0)	BA34	M_B_DQS_1_DP
425	DDR0_DQSN(0)/DDR0_DQSN(0)	BA30	M_B_DQS_4_DN
425	DDR0_DQSN(0)/DDR0_DQSN(0)	AY30	M_B_DQS_4_DP
425	DDR0_DQSN(0)/DDR0_DQSN(0)	AY26	M_B_DQS_5_DN
425	DDR0_DQSN(0)/DDR0_DQSN(0)	BA26	M_B_DQS_5_DP
425	DDR0_DQSN(0)/DDR0_DQSN(0)		M_B_DQS_5_DP 24
425	NC/DDR0_ALERT#	AW50	M_A_ALERT_N
425	NC/DDR0_PAR	AT52	M_A_PARITY
425	DDR_VREF_CA	AY67	
425	DDR_VREF_DQ	AY68	
425	DDR_VREF_DQ	BA67	
425	DDR_VTT_CTRL	AW67	DDR_VTT_CTRL



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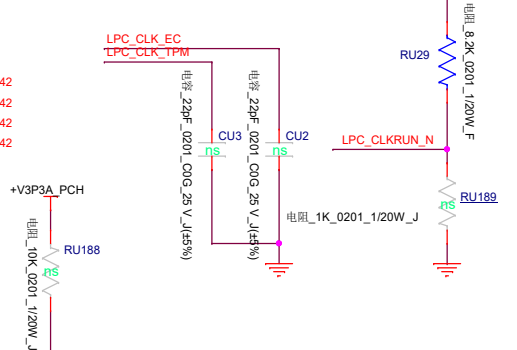
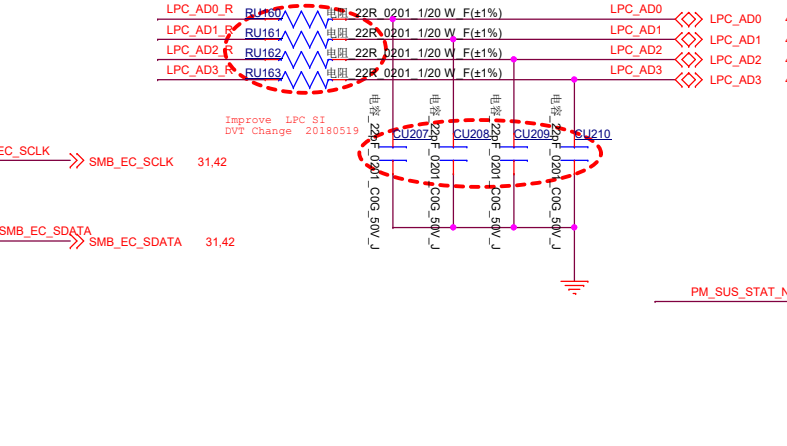
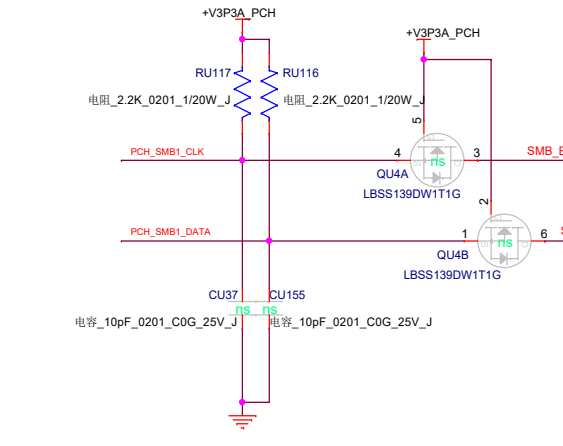
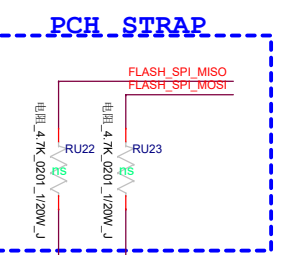
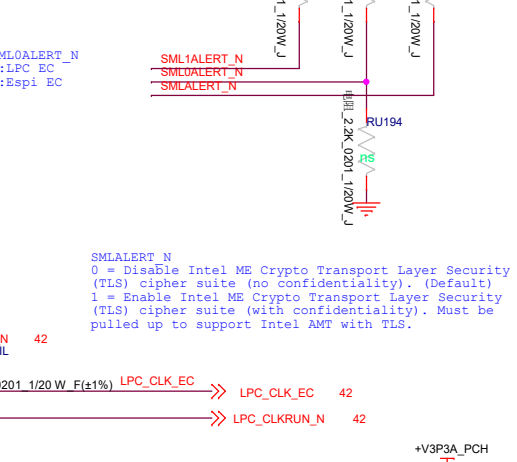
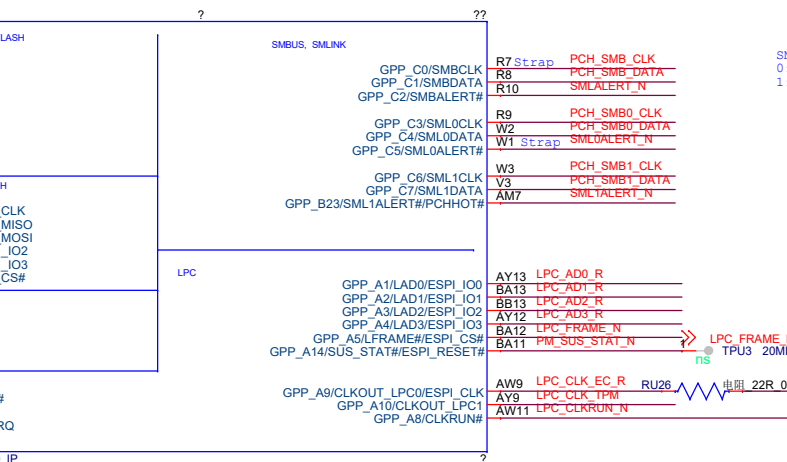
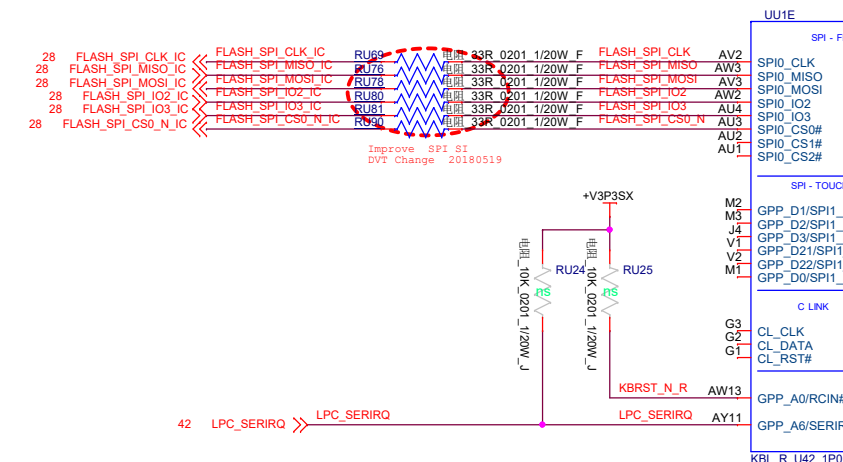
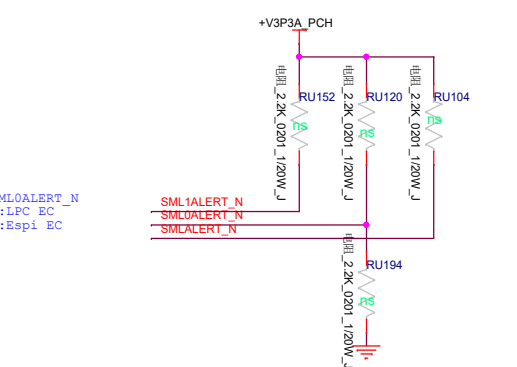
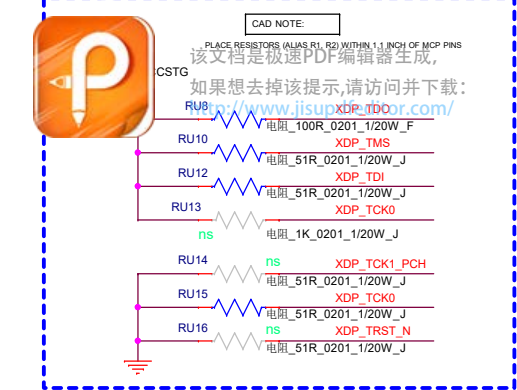
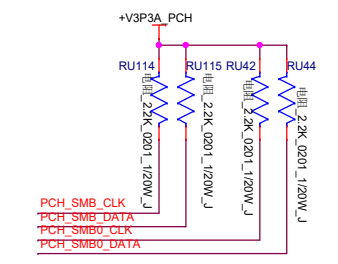
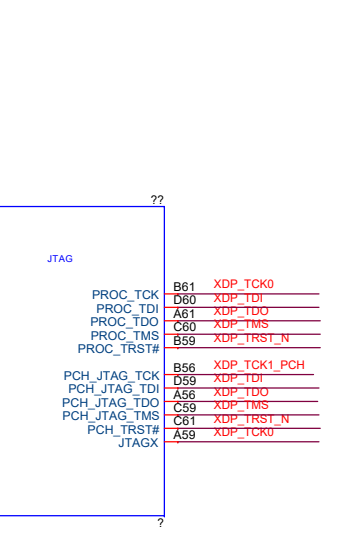
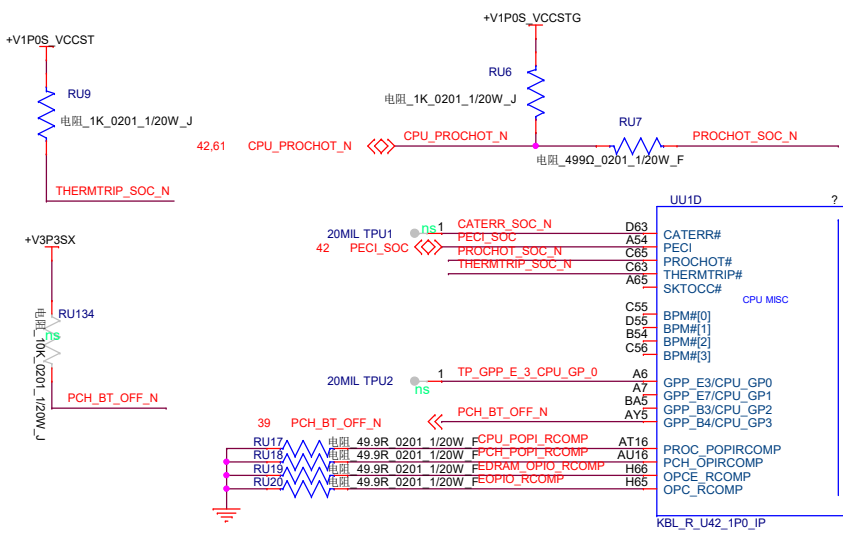


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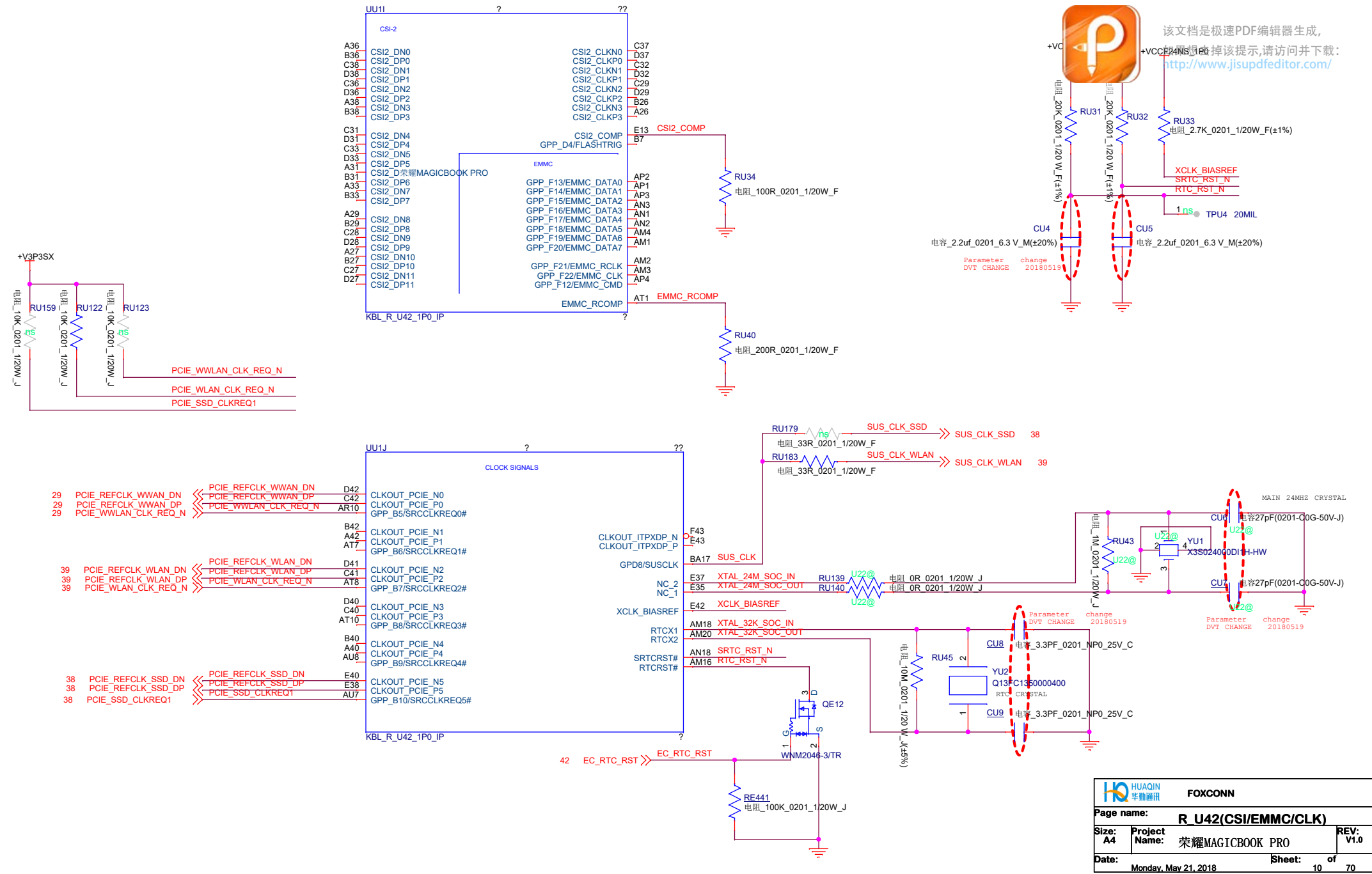
DISPLAY PORT STRAP

	Strap	How to Enable Port	How to Enable Port
DDI Port 1	DDPB_CTRLDATA (L12)	Pull up to 3.3 V	No Connect
DDI Port 2	DDPC_CTRLDATA (N8)	Pull up to 3.3 V	No Connect

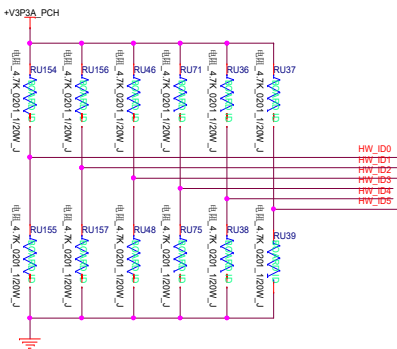


CAD NOTE

PLACE RESISTORS (ALIAS R1, R2) WITHIN 1.1 INCH OF MCP PINS
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		FOXCONN	
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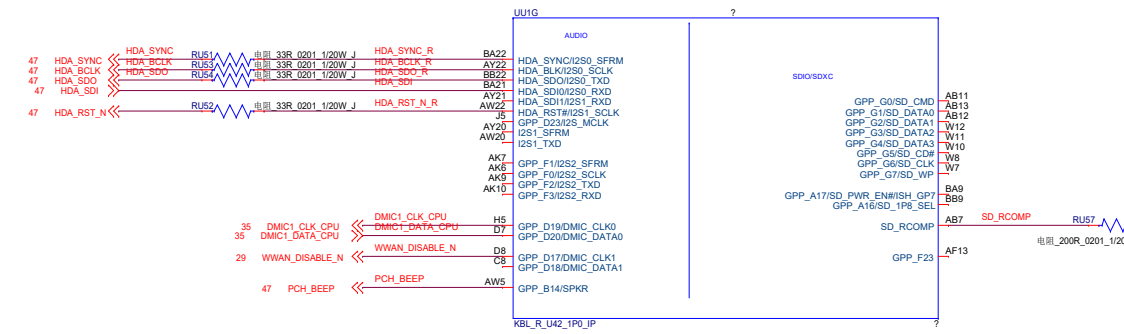
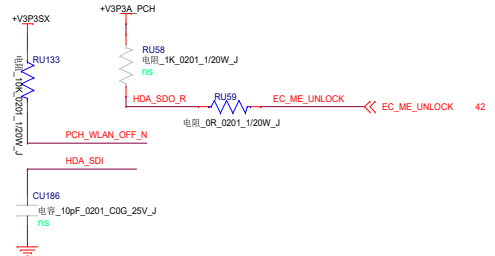
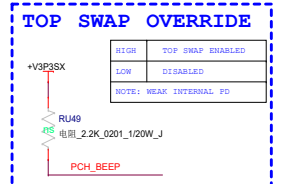
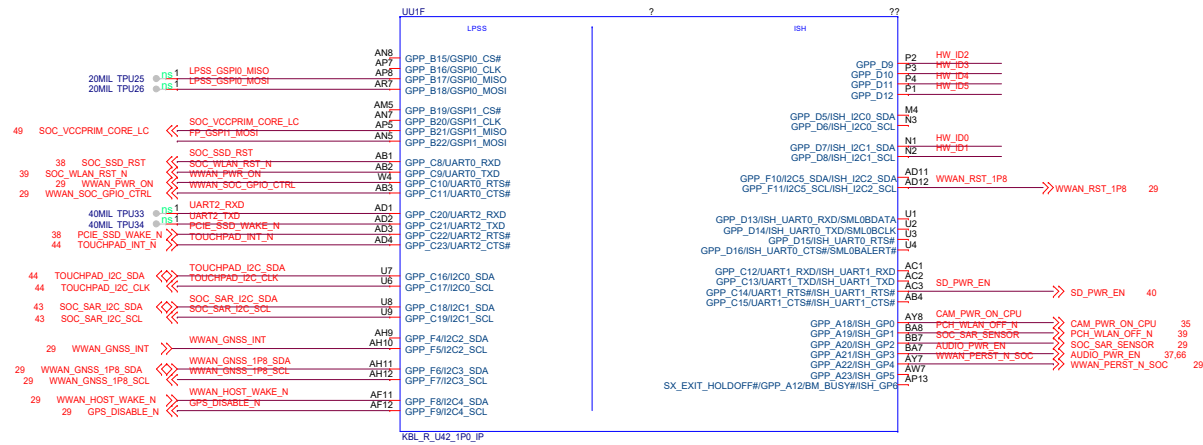
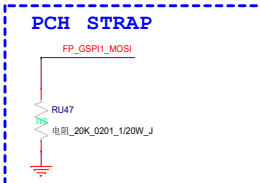
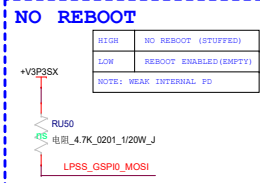


Memory	Channel	HW_ID5	HW_ID4	HW_ID2	HW_ID1	HW_ID0
Hynix 4G 2400	single			RU48	RU49	RU155
Micron 4G 2666	single			RU46	RU156	RU164
Hynix 4G 2666	single			0	0	1
Micron 8G 2666	Dual	0-RU39 (U42)	0-RU38 (LTE+WIFI)	0	0	1
Hynix 8G 2400	Dual	1-RU37 (U22)	1-RU36 (WIFI ONLY)	0	1	0
Hynix 8G 2666	Dual			0	1	0
Hynix 16G 2400	Dual			0	1	1
Micron 16G 2400	Dual			0	1	1
Micron 16G 2666	Dual			1	0	0



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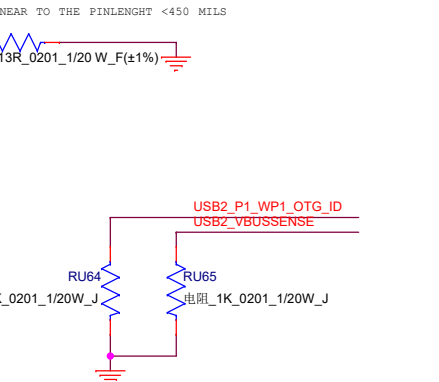
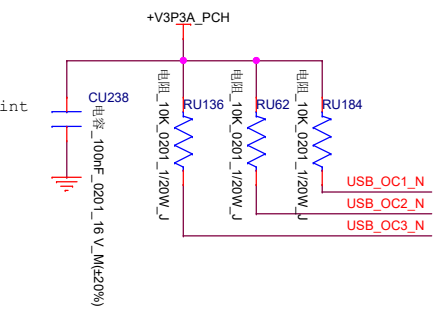
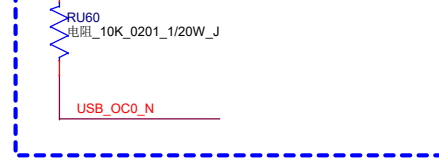
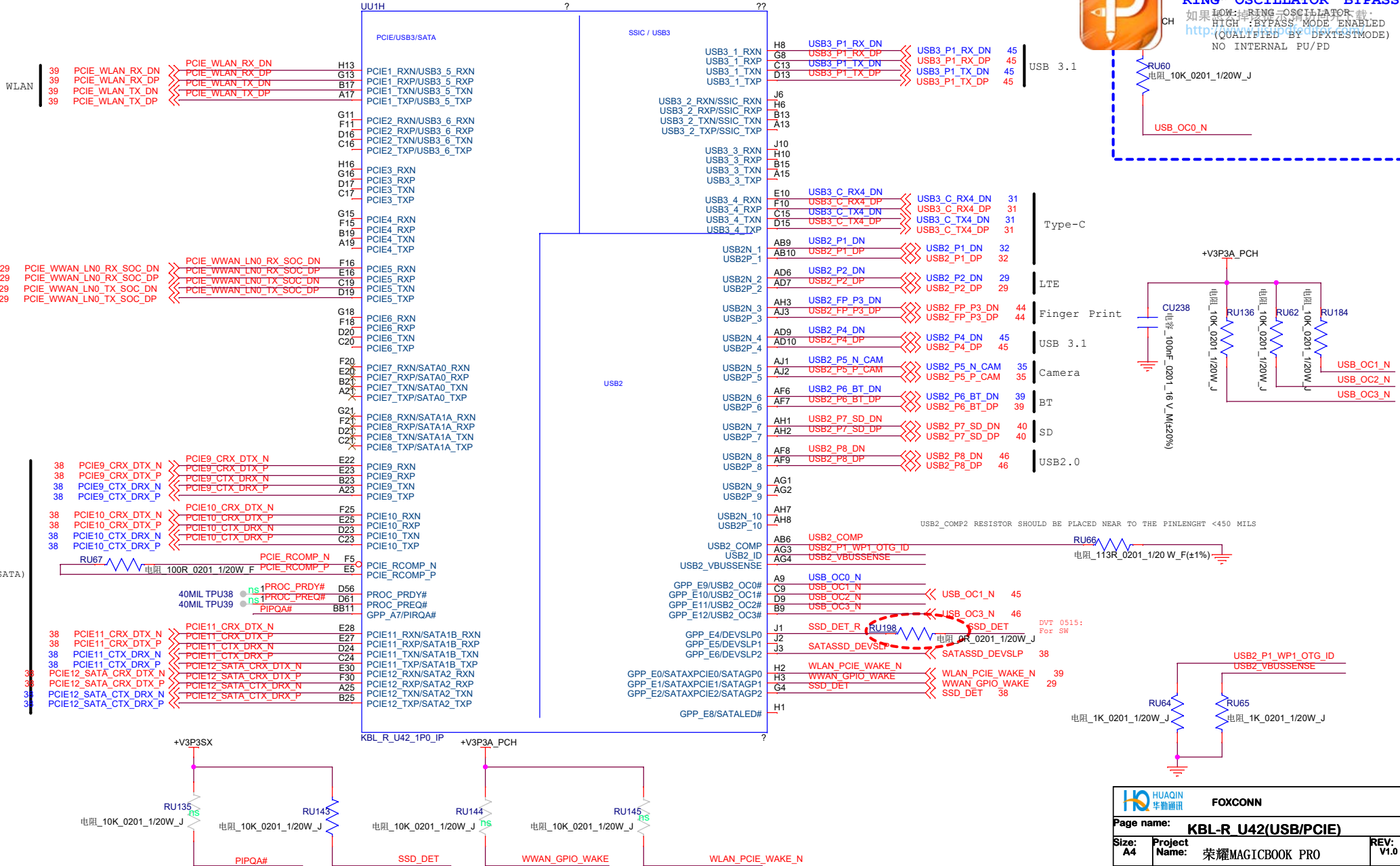
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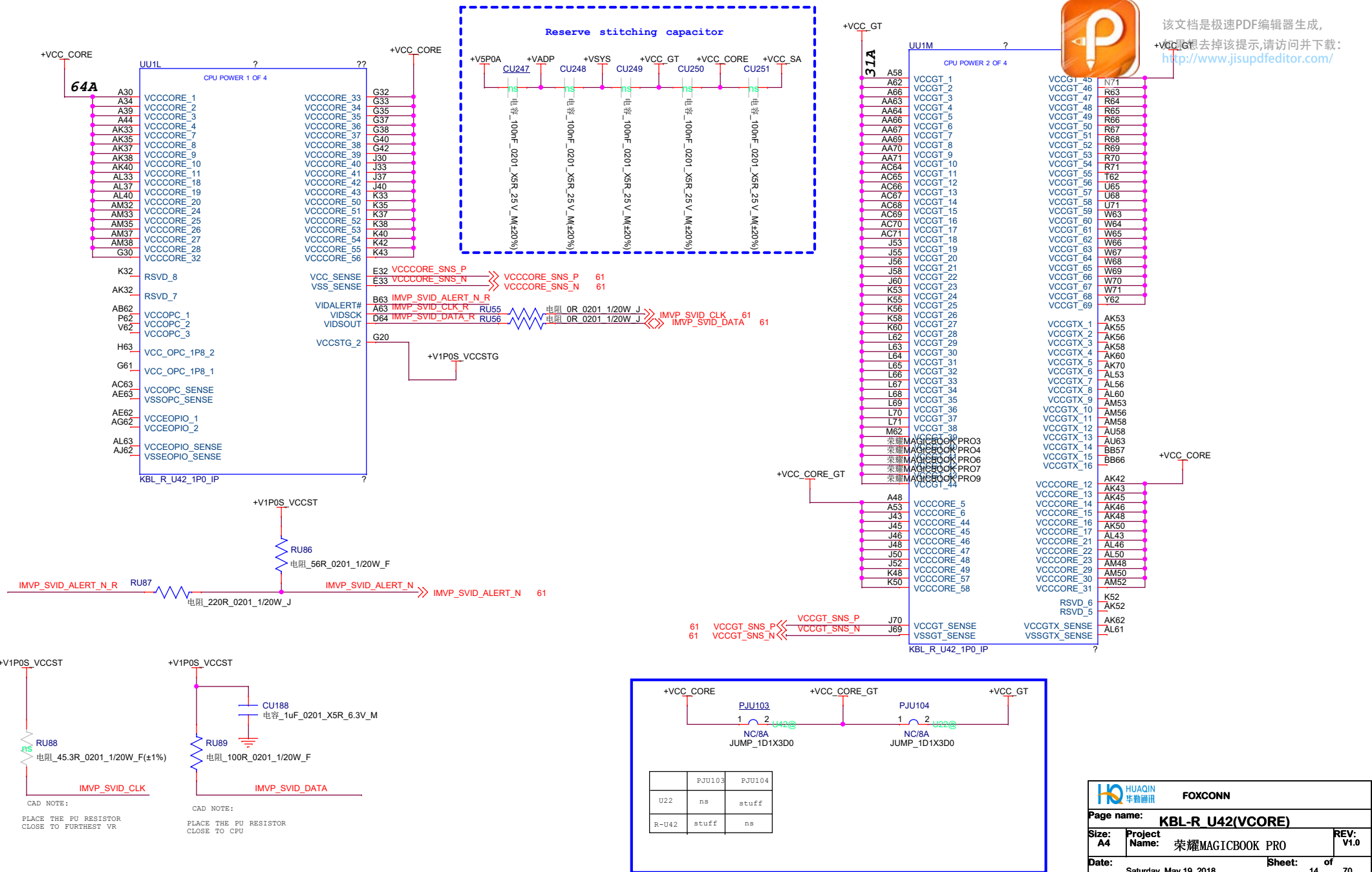


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RING OSCILLATOR BYPASS
LOW: RING OSCILLATOR
HIGH: BYPASS MODE ENABLED
(QUALIFIED BY: DFXTSTMODE)
NO INTERNAL PU/PD



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Page name: KBL-R U42(USB/PCIE)			
Size: A4	Project Name: 荣耀MAGICBOOK PRO	REV: V1.0	
Date: Saturday, May 19, 2018	Sheet: 12	of	70



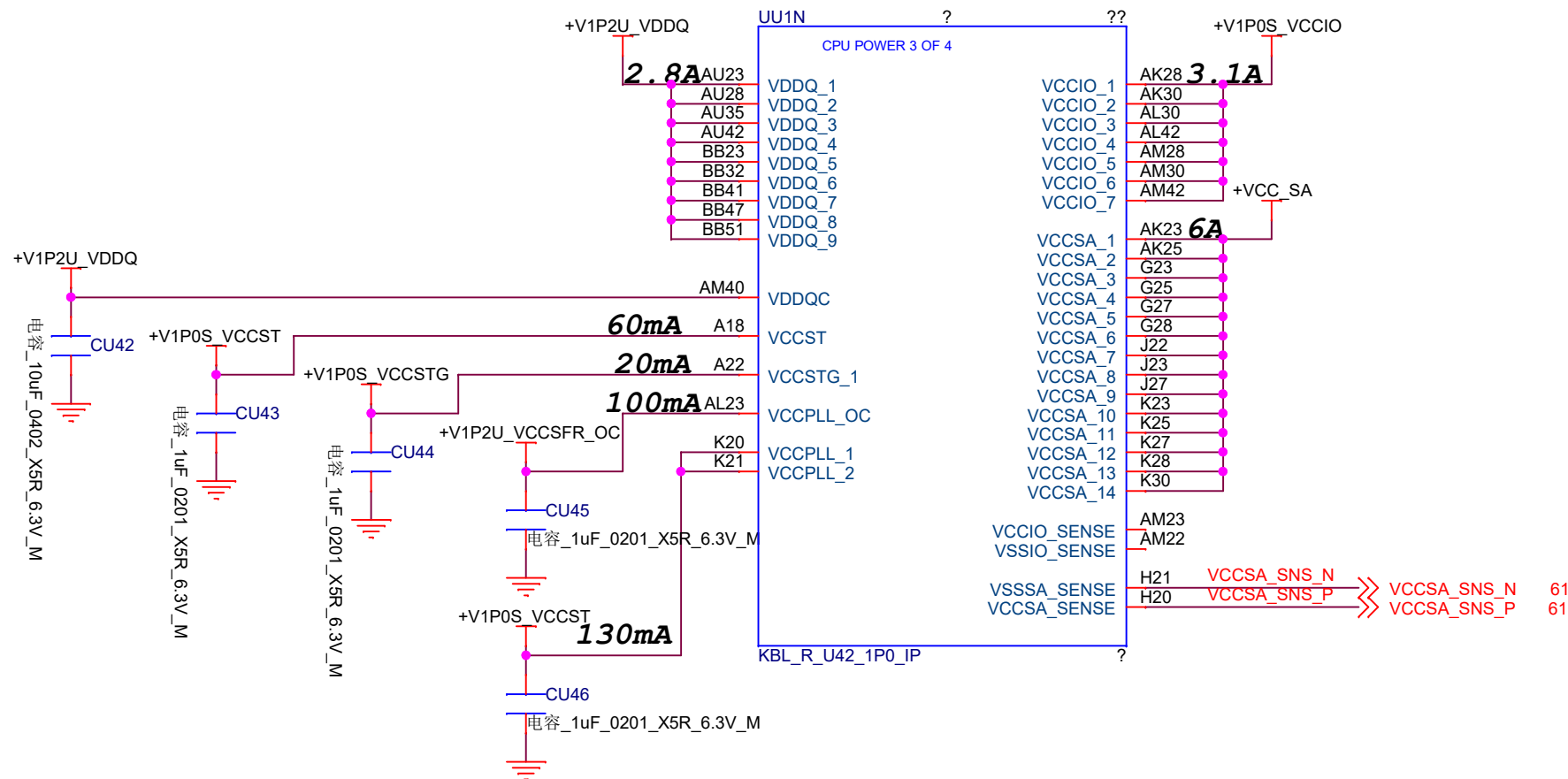
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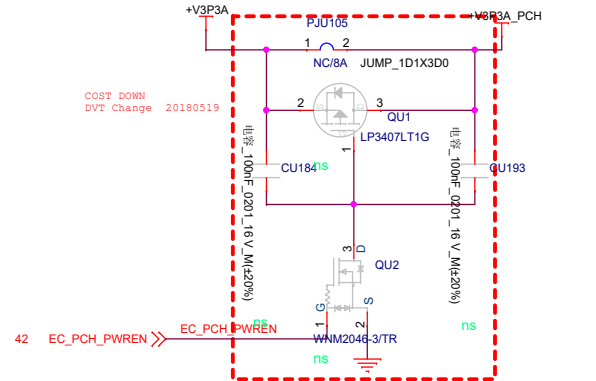
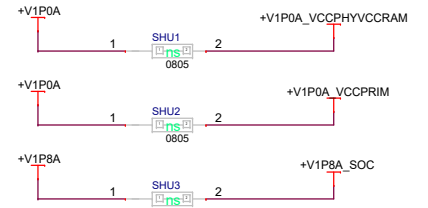
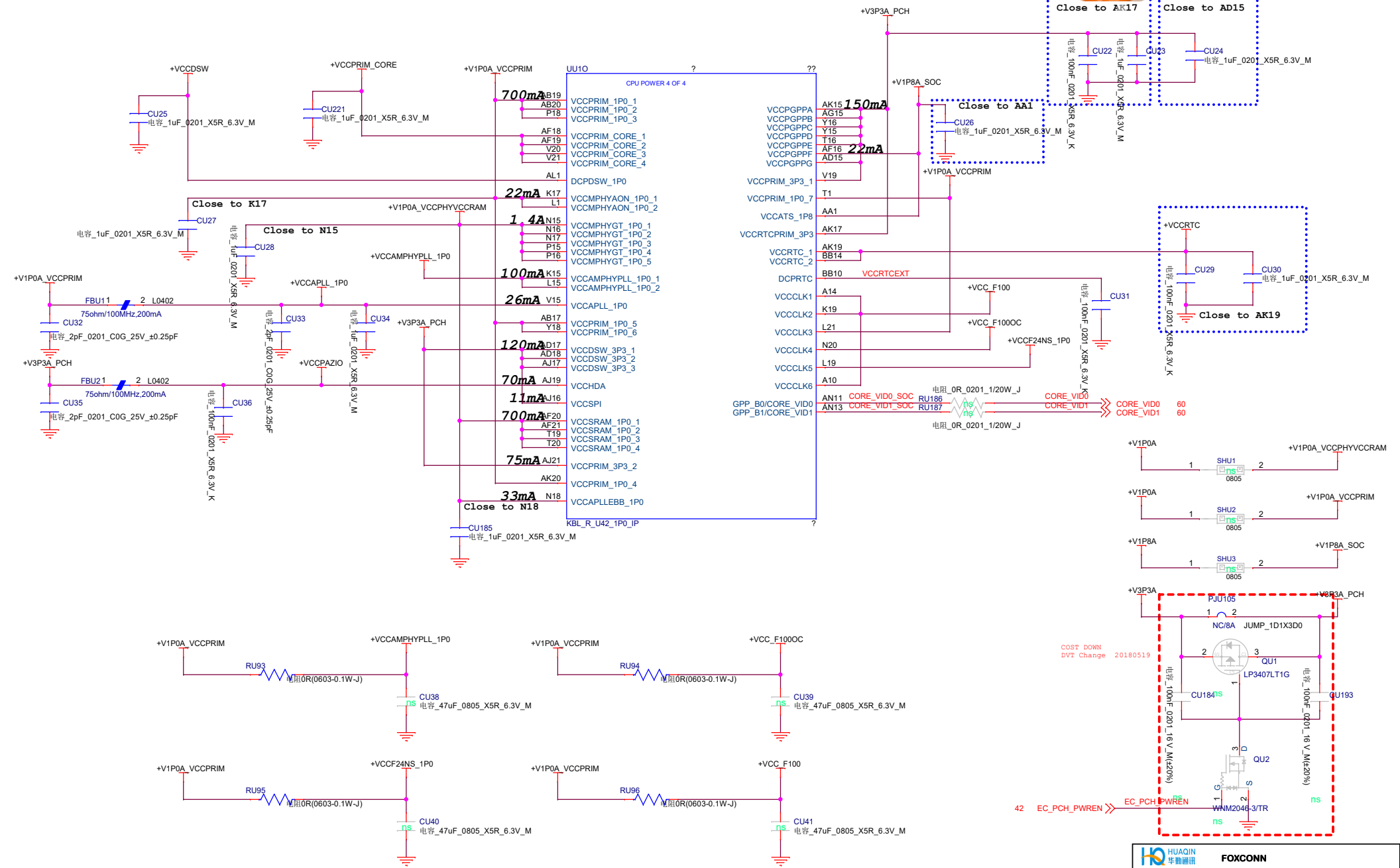
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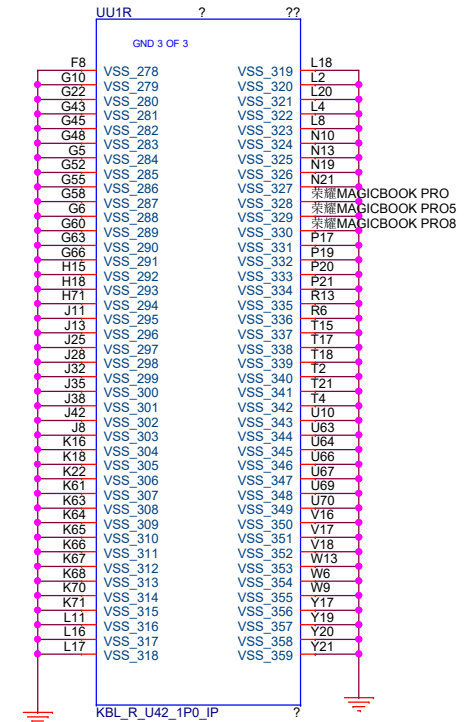
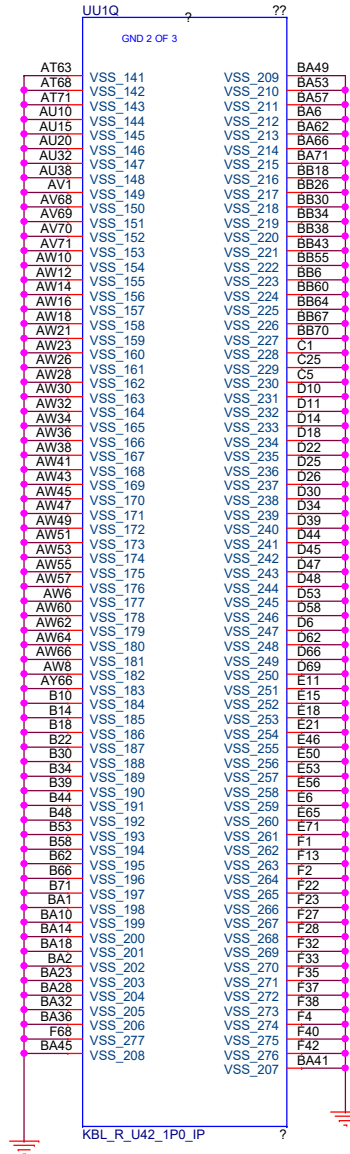
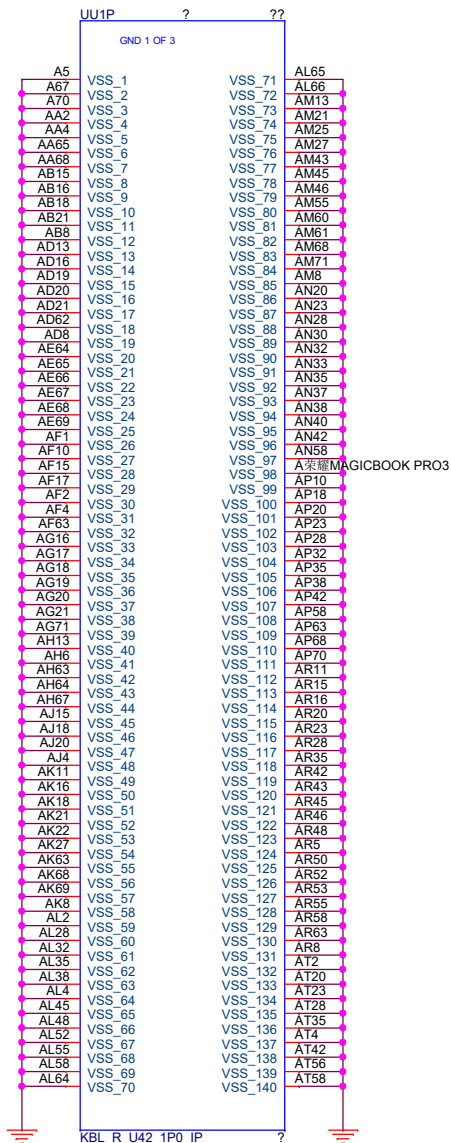
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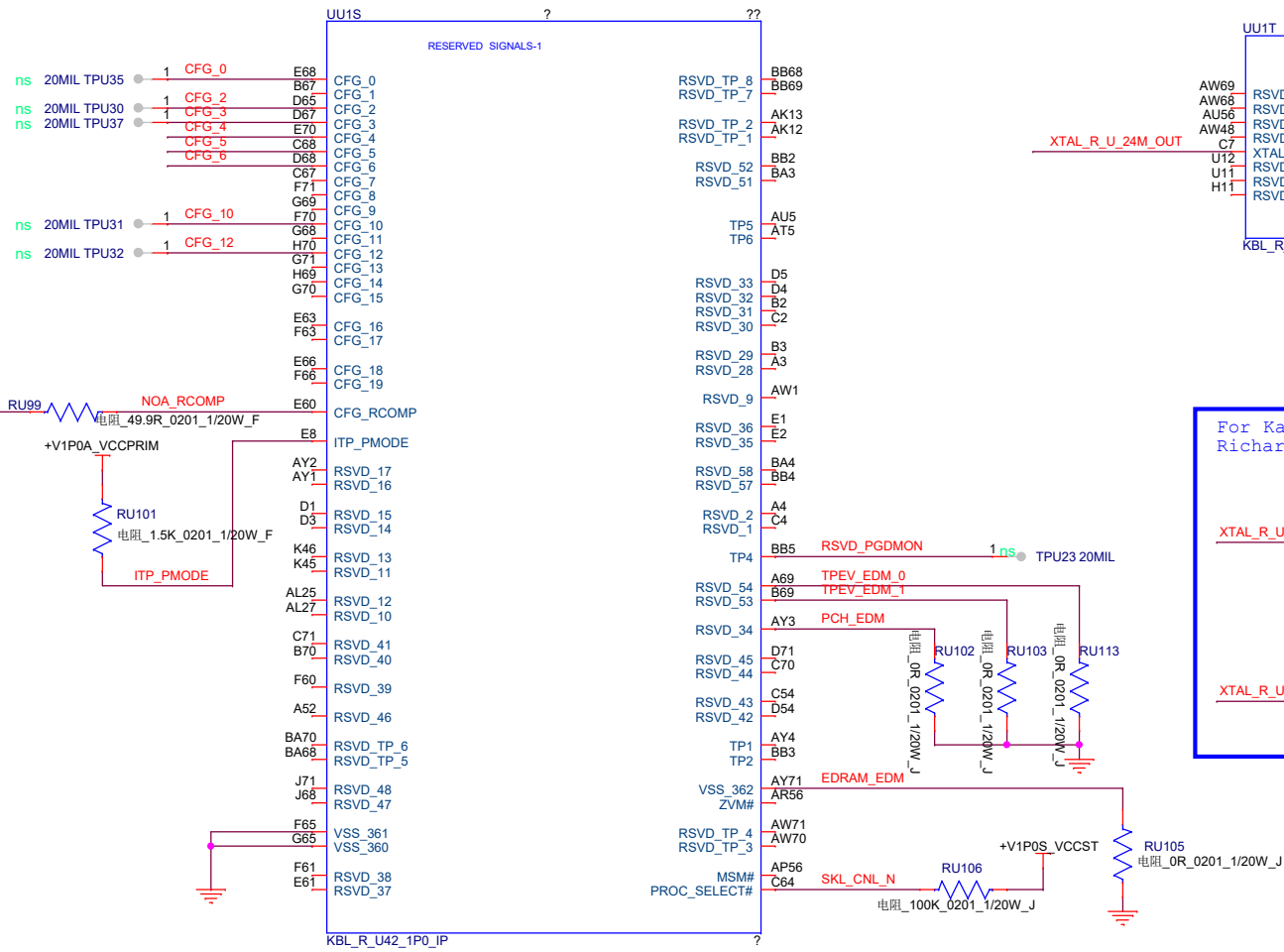


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Date: Saturday, May 19, 2018		Sheet: 15 of 70	

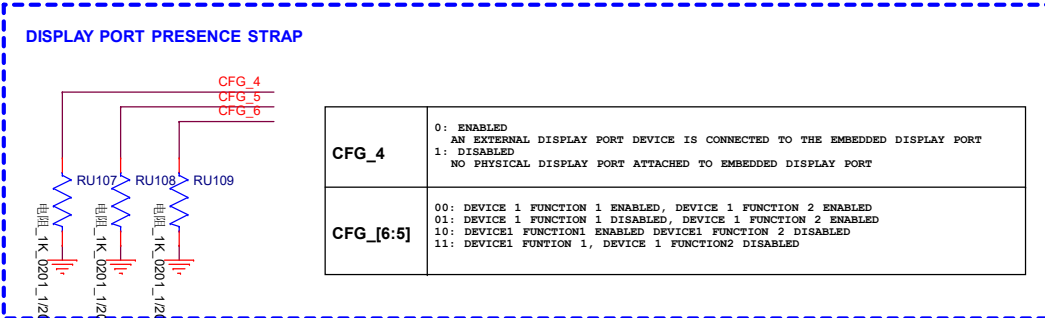
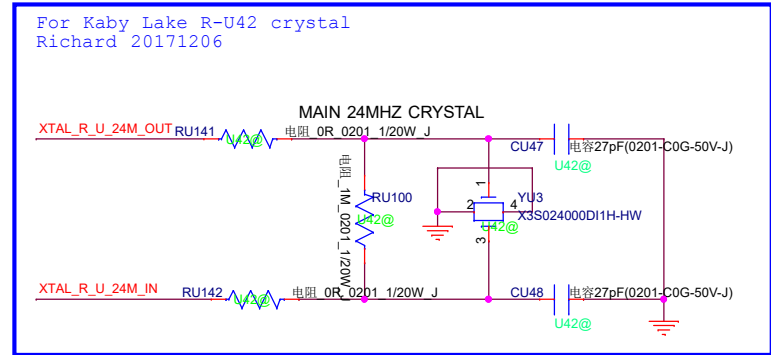


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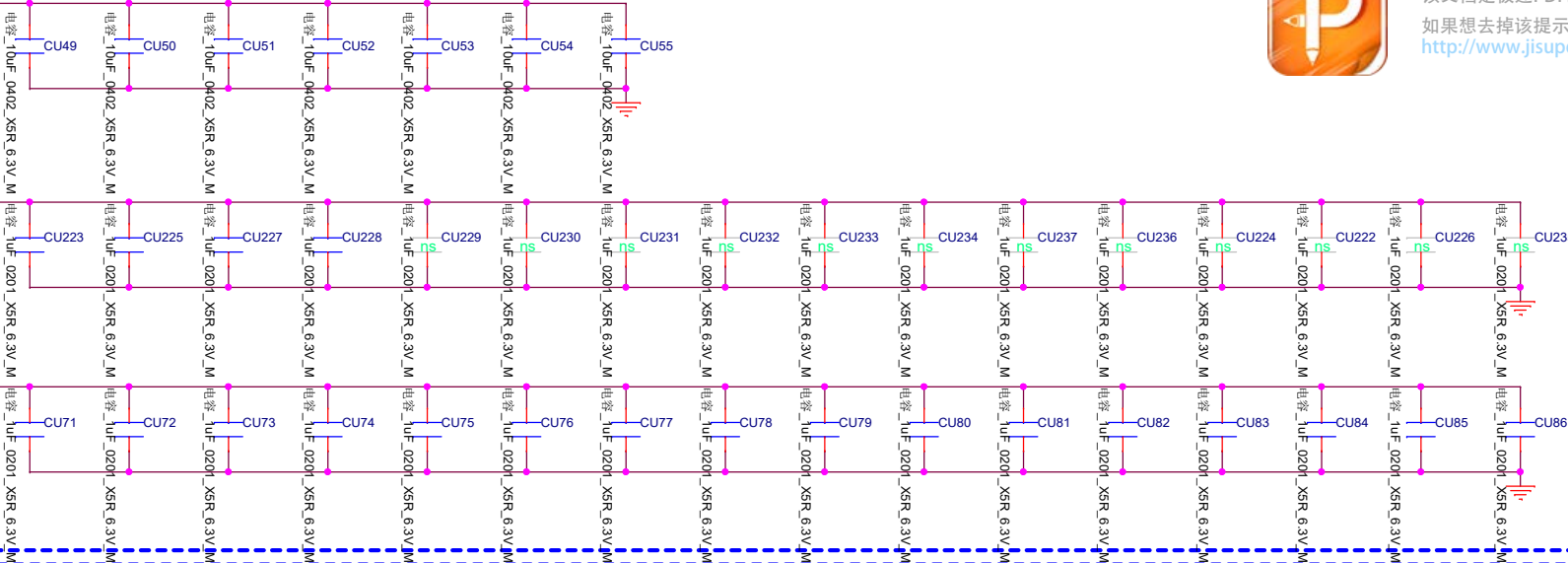
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Date: Saturday, May 19, 2018	Sheet: 18	of	70



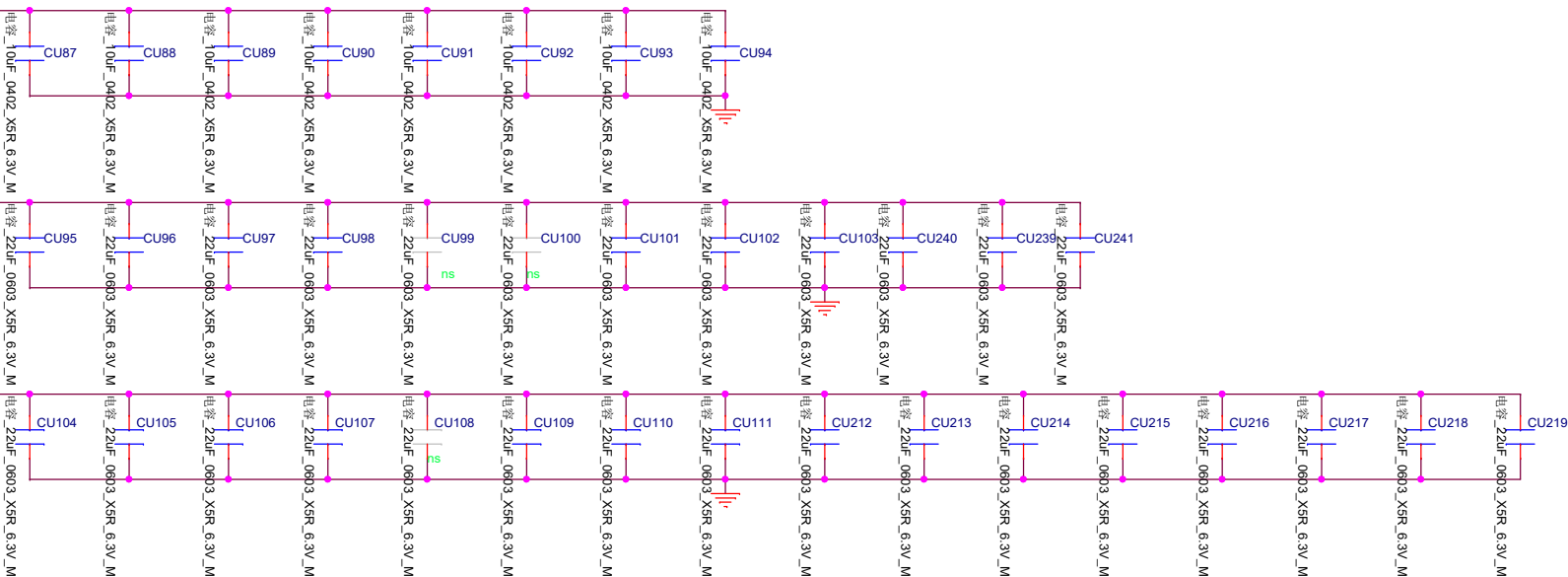
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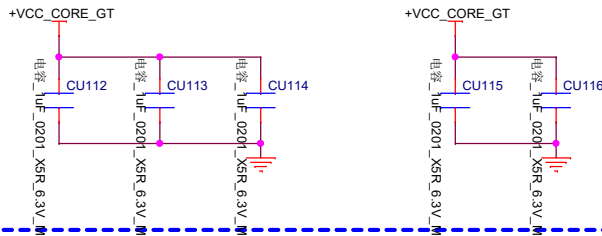
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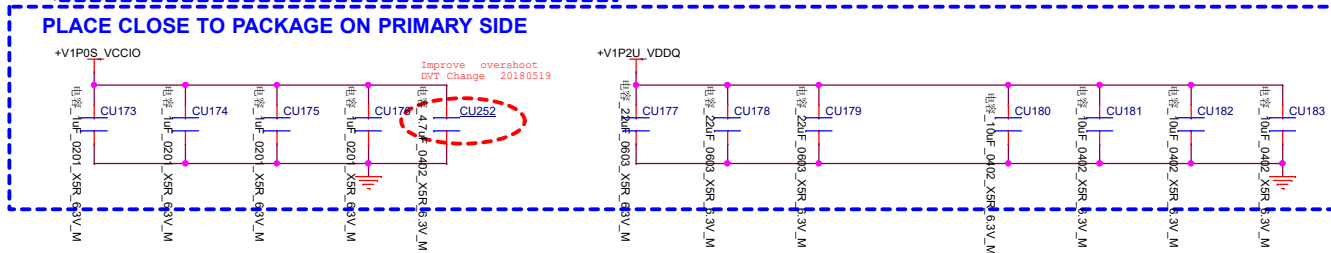
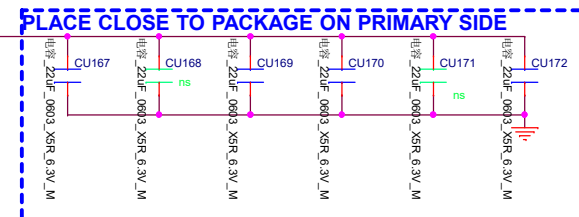
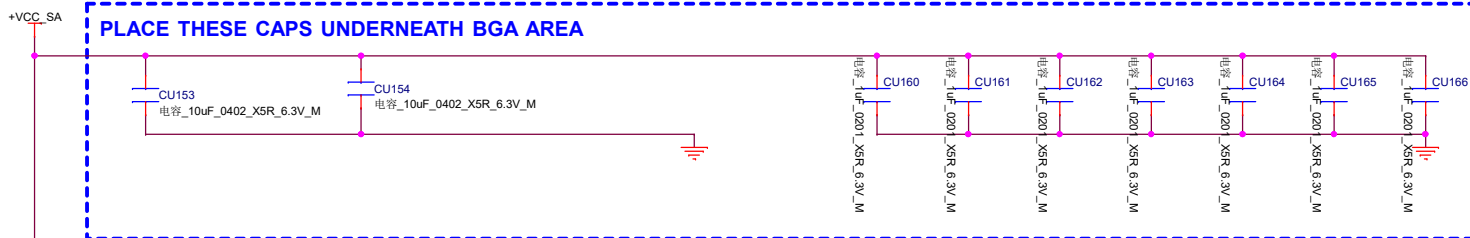
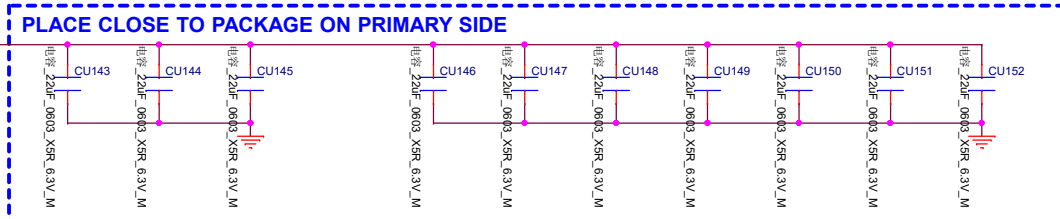
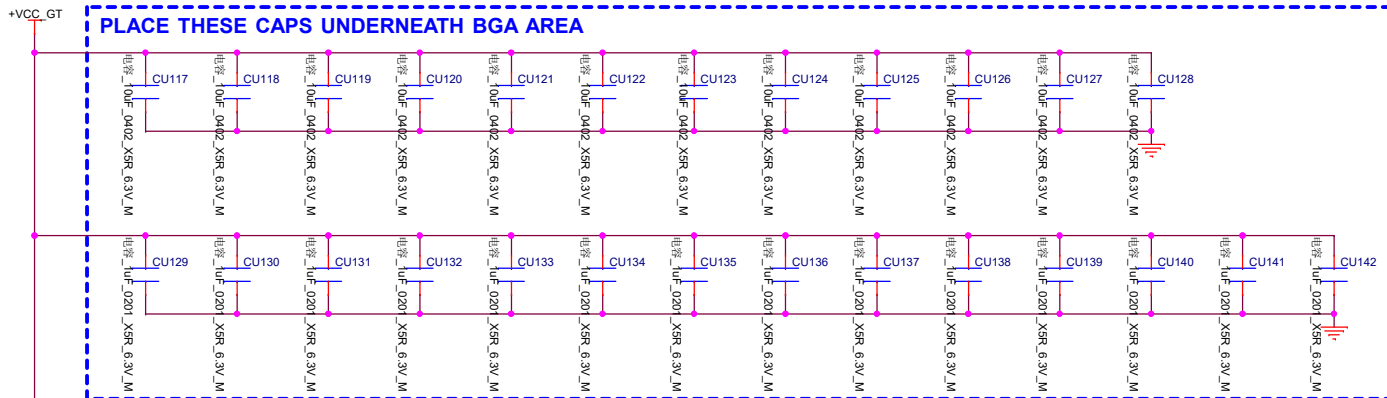
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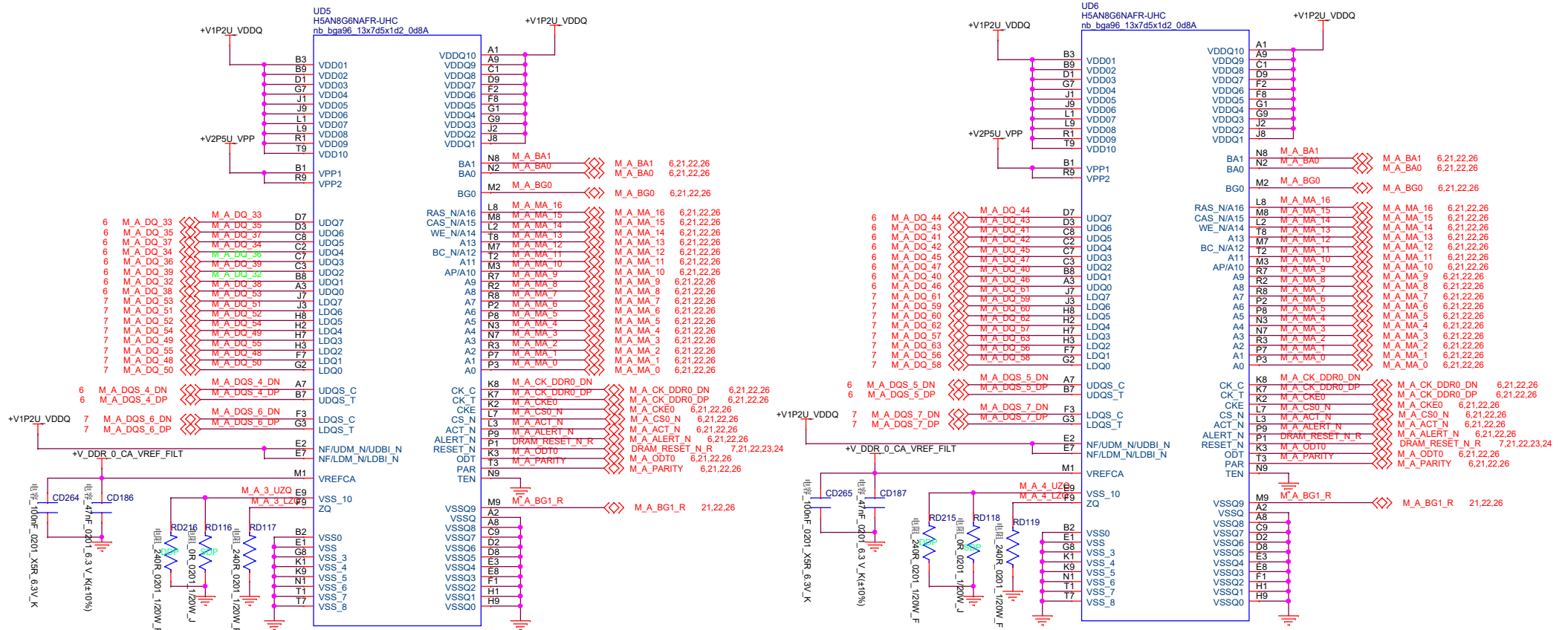


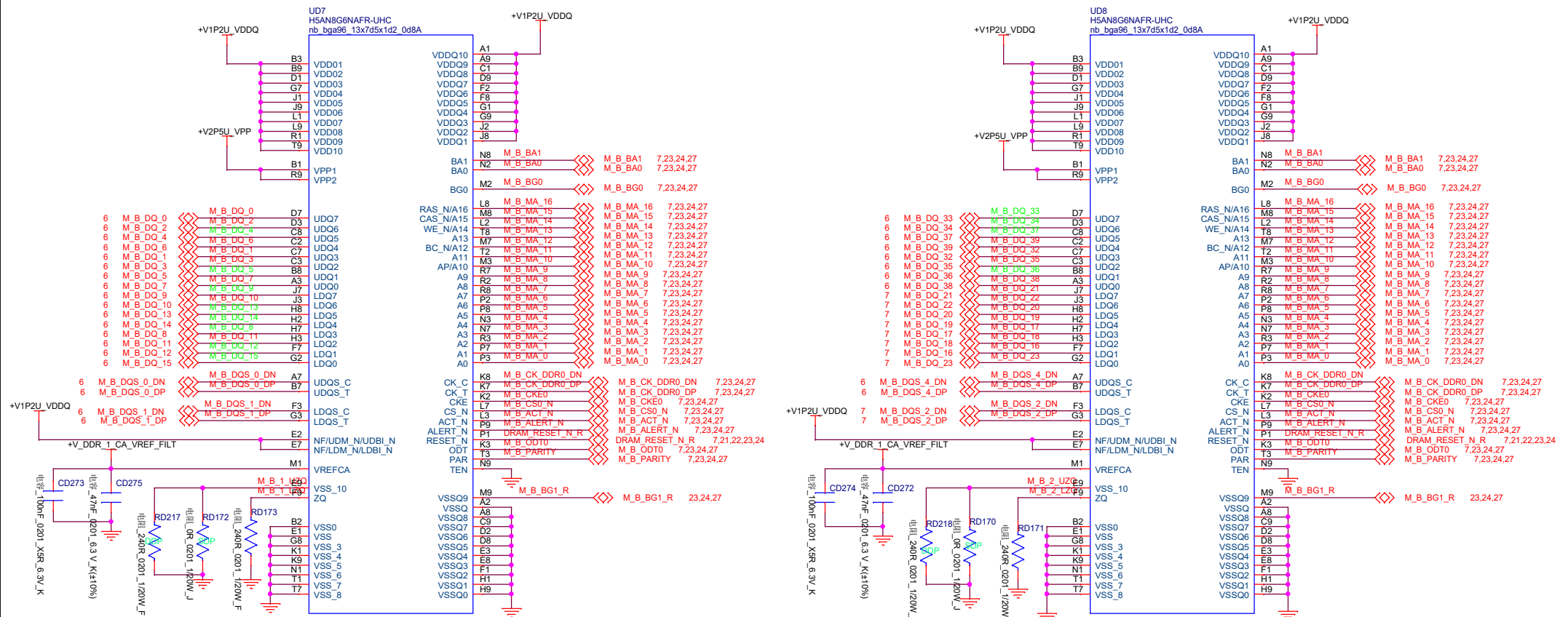
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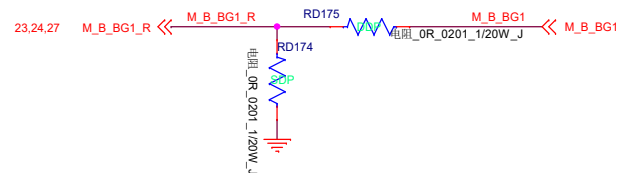
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Date: Saturday, May 19, 2018	Sheet: 19	of 70	

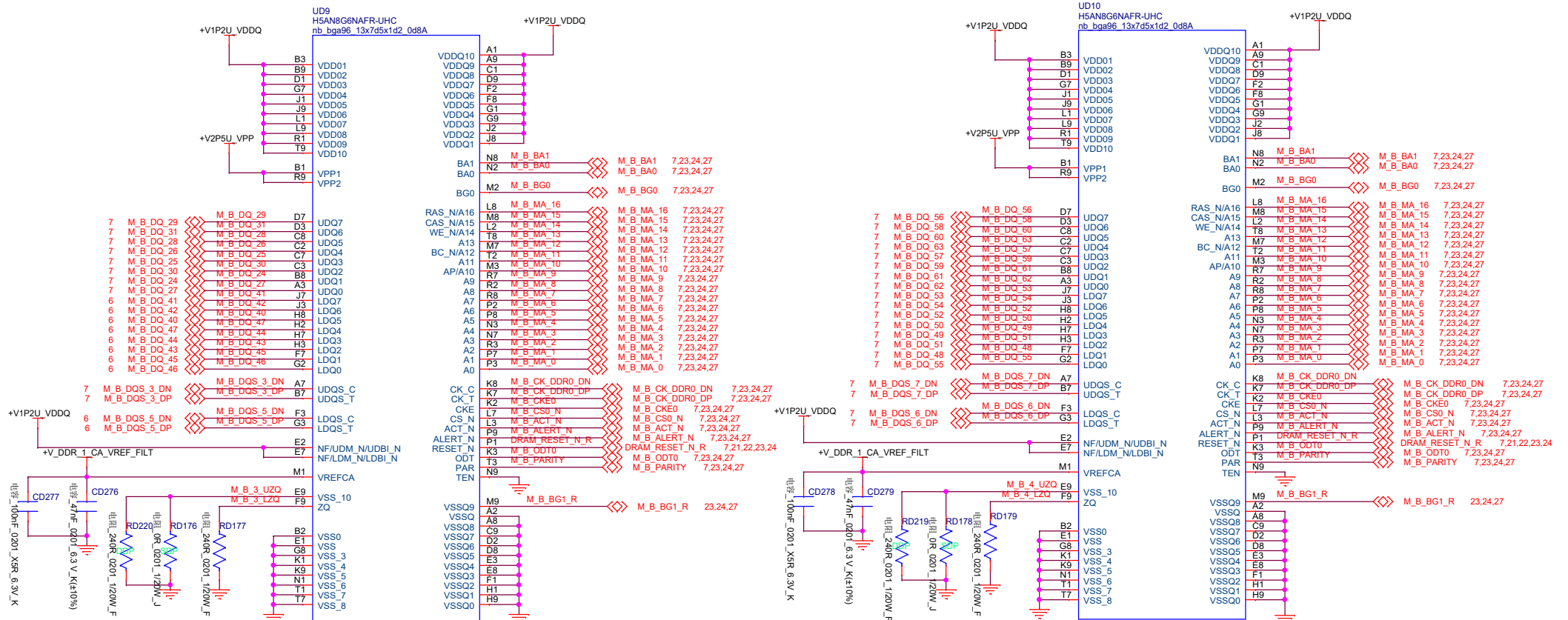




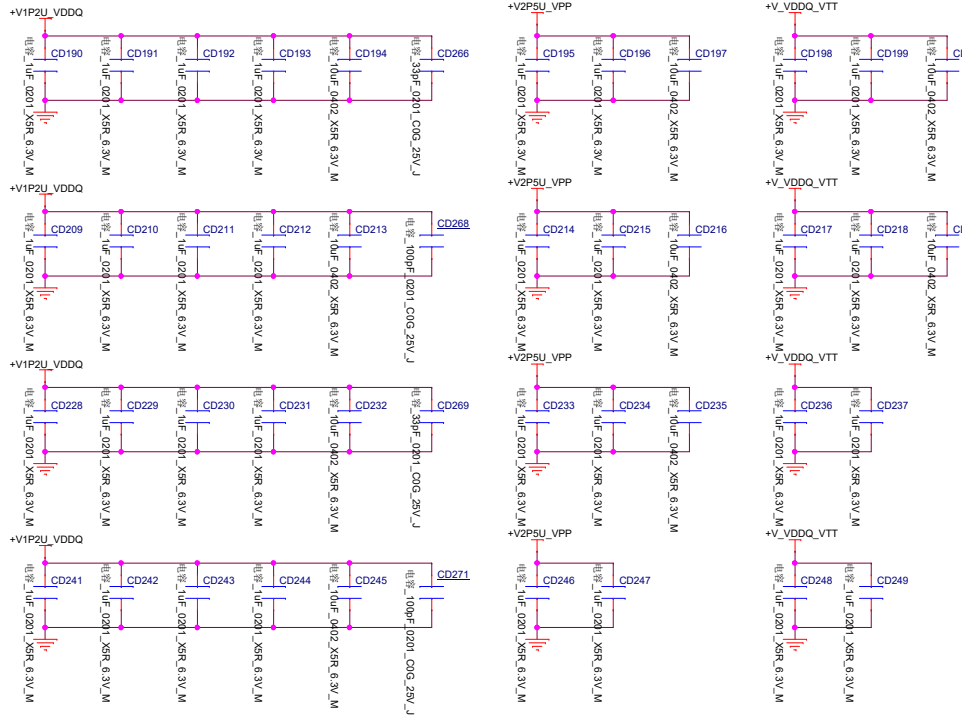


NOTE: TO BE STUFF FOR DUAL ZQ PARTS

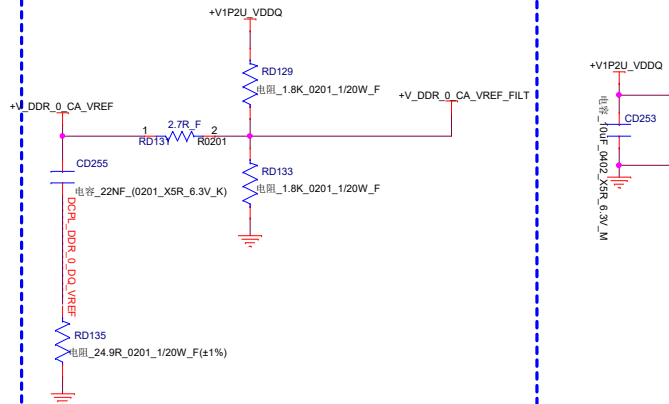




DECOUPLING CAPACITORS FOR DDR CHANNEL A



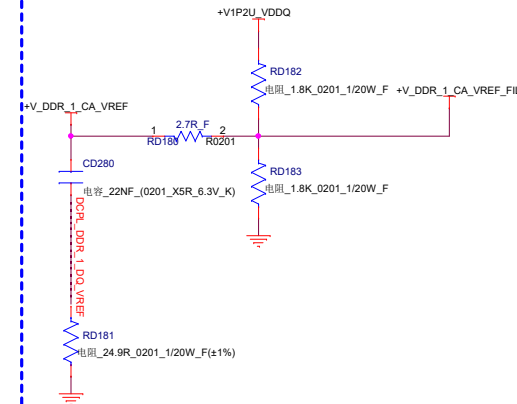
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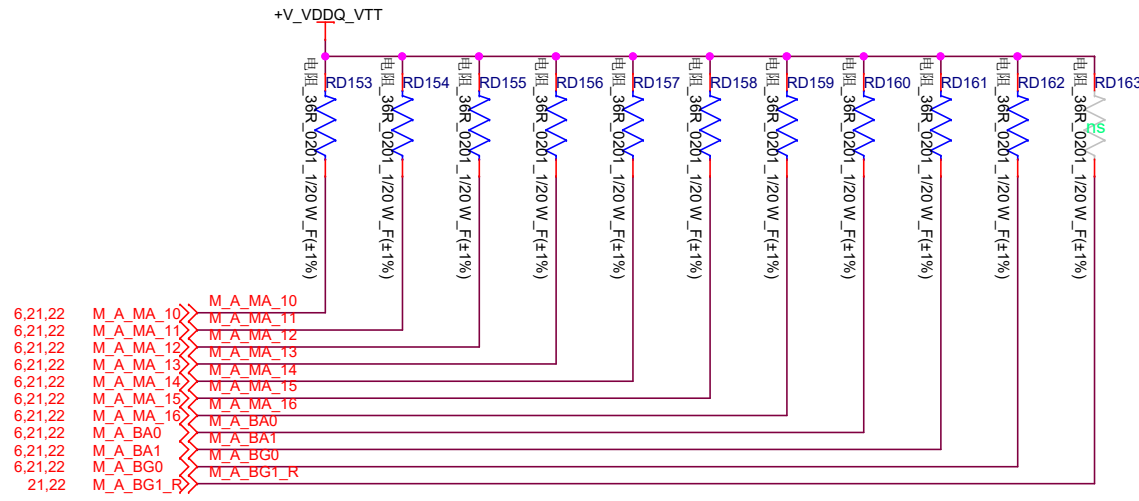
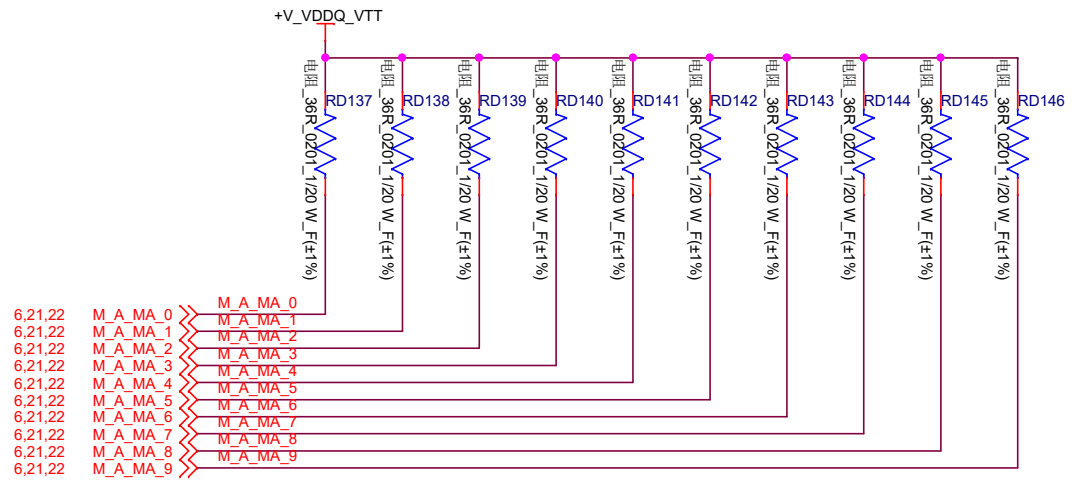
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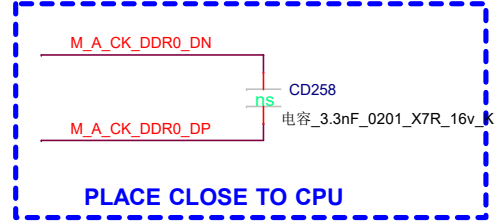
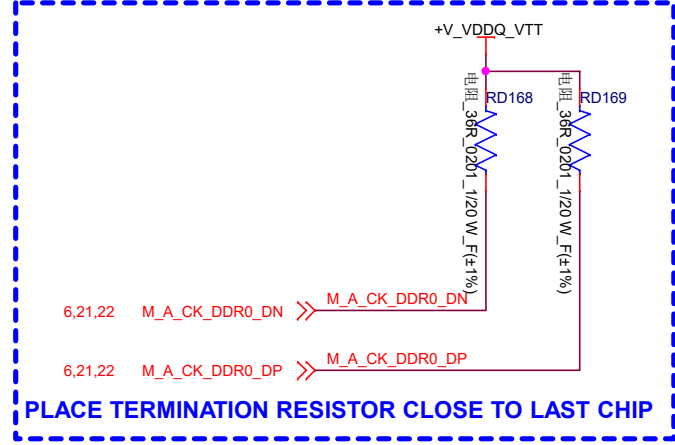
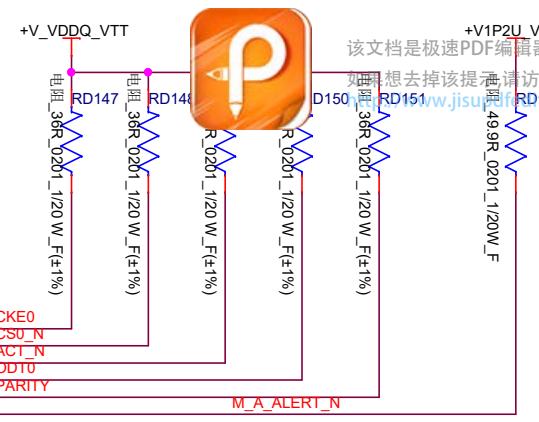
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MEMORY TERMINATIONS

CHANNEL A



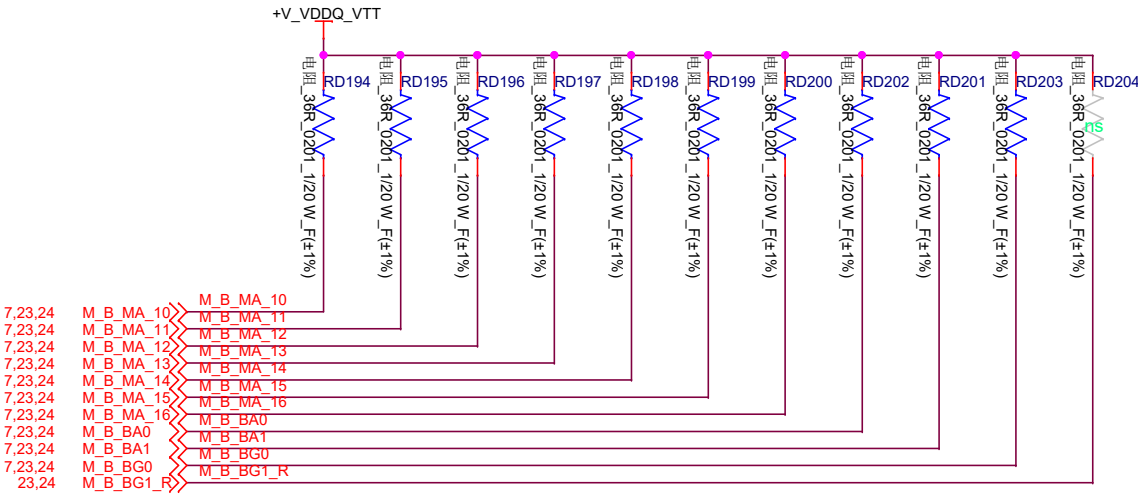
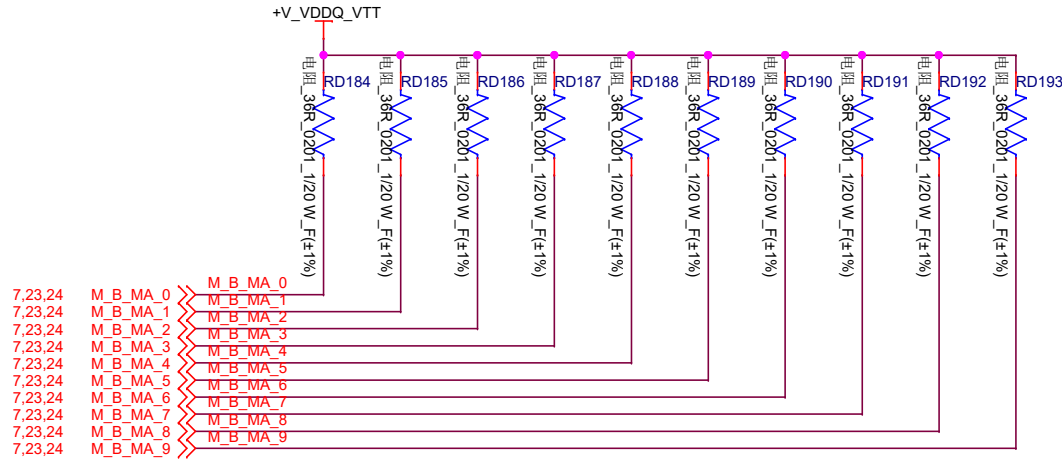
- 6,21,22 M_A_CKE0
- 6,21,22 M_A_CS0_N
- 6,21,22 M_A_ACT_N
- 6,21,22 M_A_ODT0
- 6,21,22 M_A_PARITY
- 6,21,22 M_A_ALERT_N



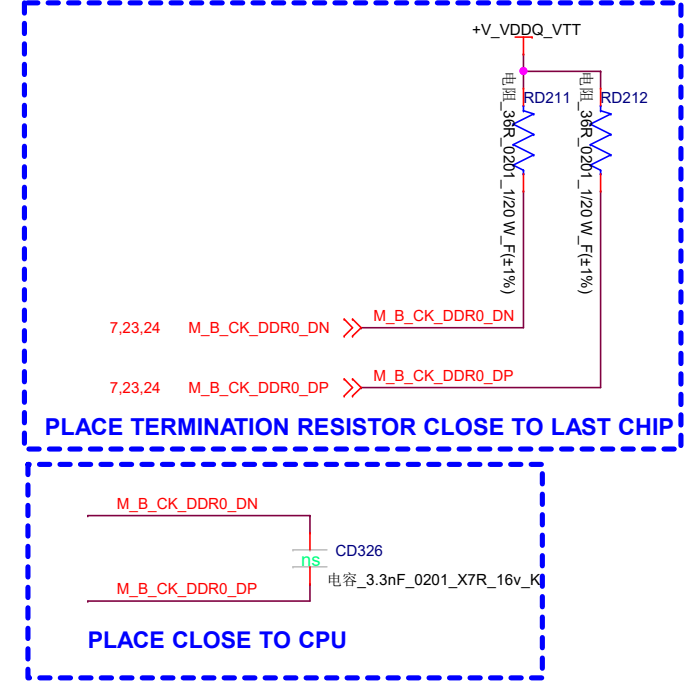
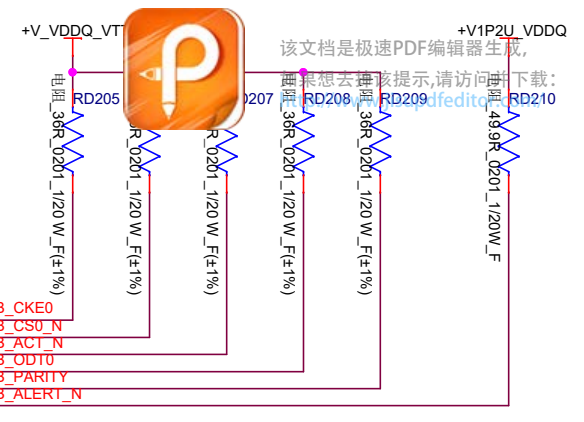
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Date: Saturday, May 19, 2018	Sheet: 26	of 70	

MEMORY TERMINATIONS

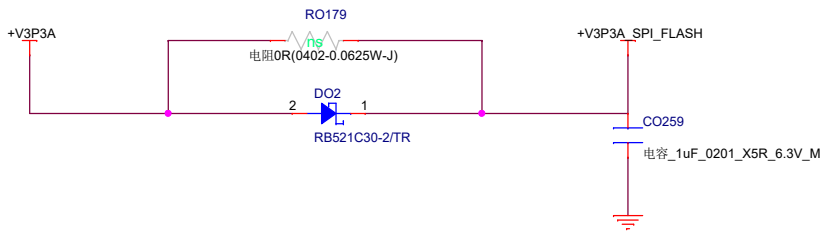
CHANNEL B



- 7,23,24 M_B_CKE0 M_B_CKE0
- 7,23,24 M_B_CS0_N M_B_CS0_N
- 7,23,24 M_B_ACT_N M_B_ACT_N
- 7,23,24 M_B_ODT0 M_B_ODT0
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- 7,23,24 M_B_ALERT_N M_B_ALERT_N



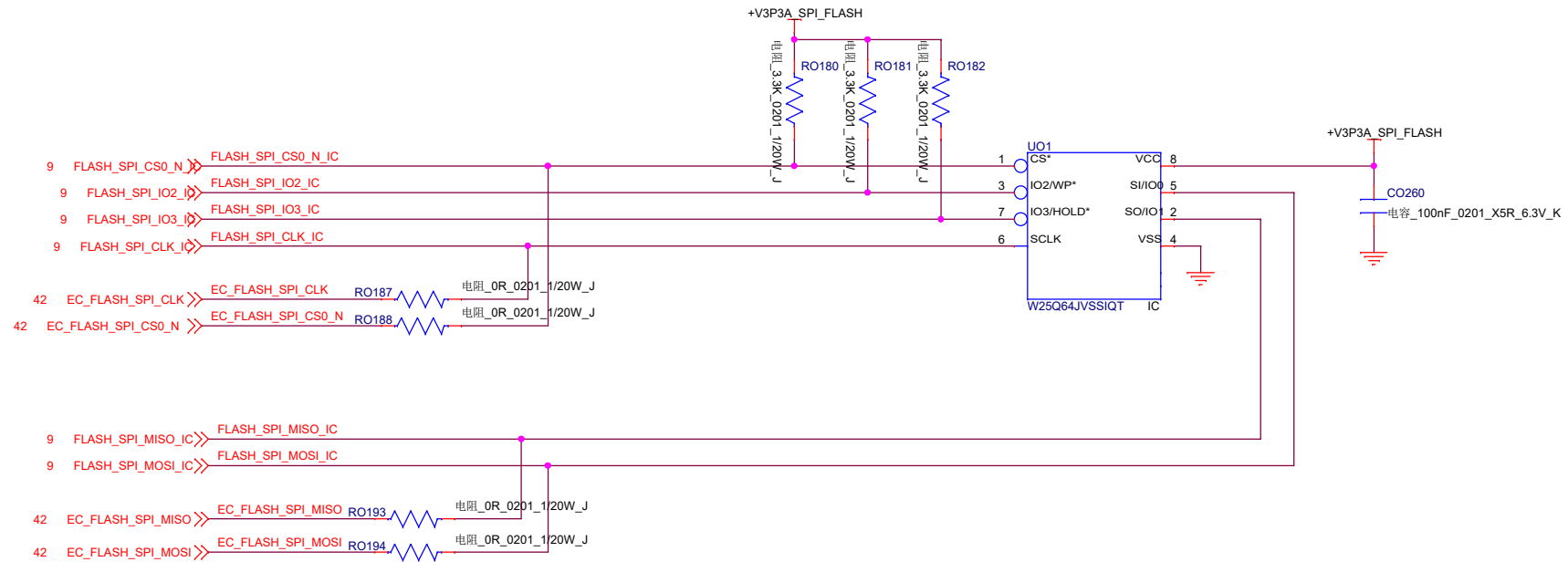
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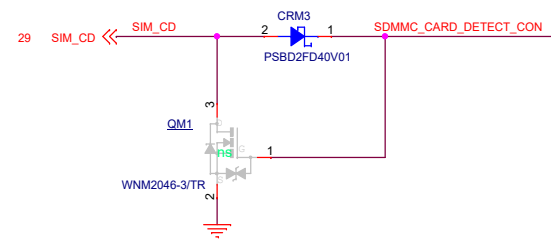
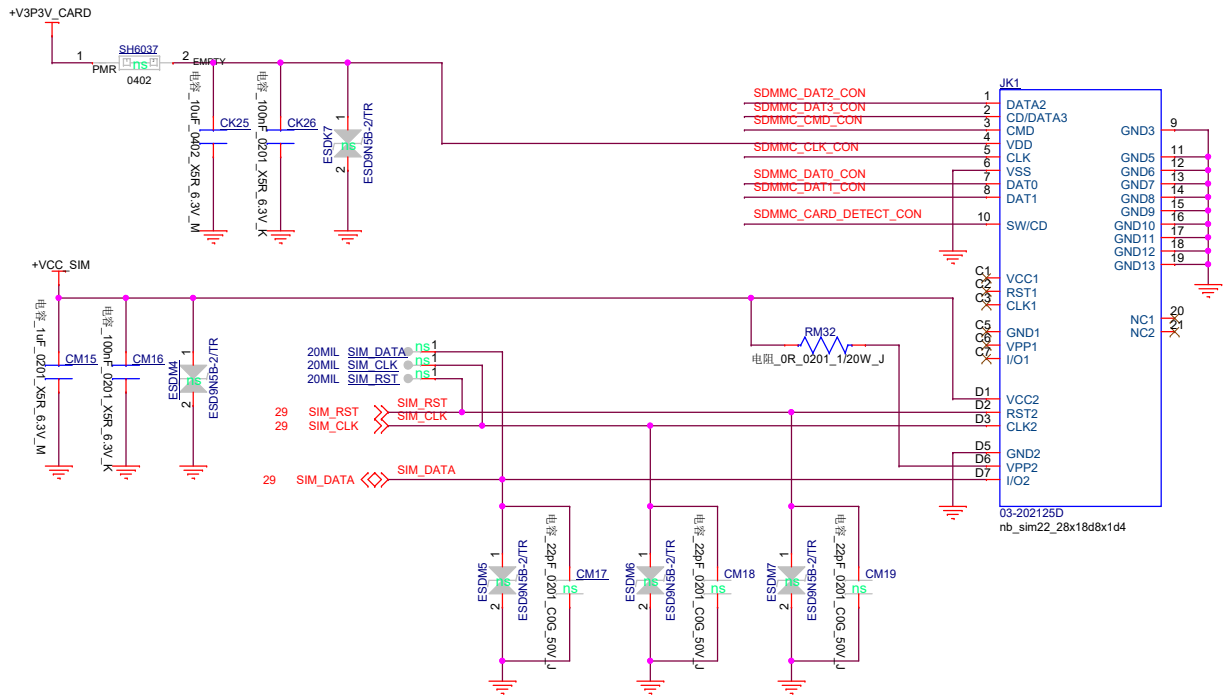
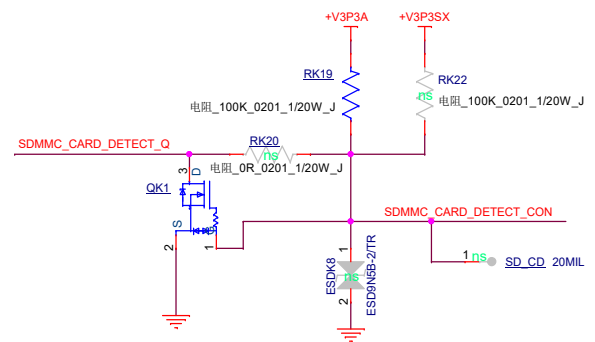
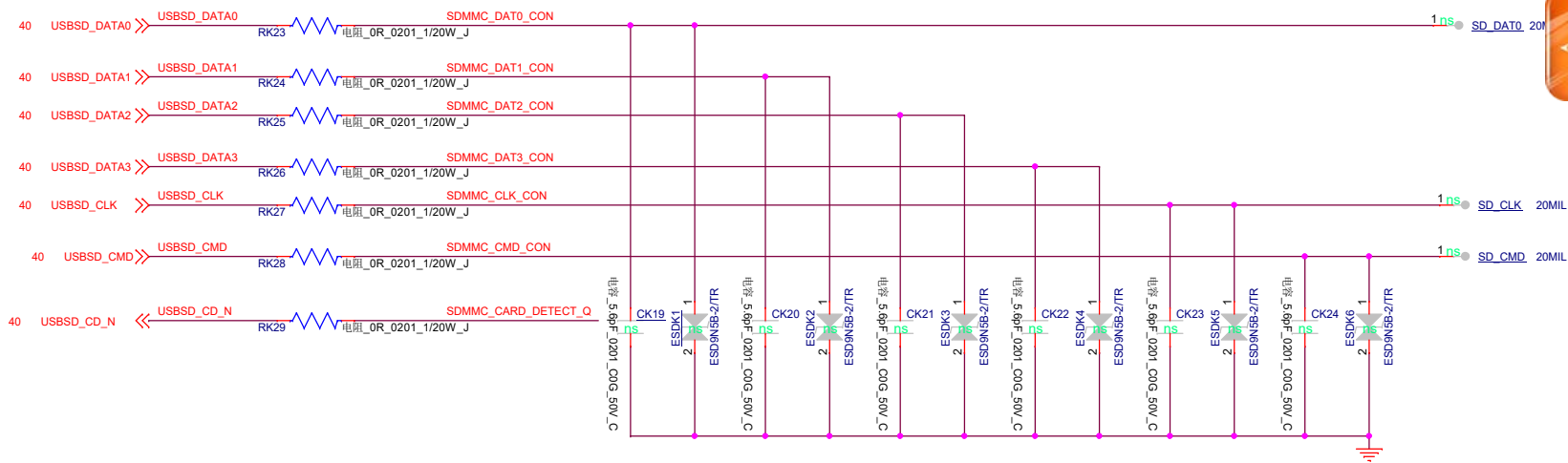
FOR pro

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FLASH_SPI_CLK_IC	40MIL
FLASH_SPI_MISO_IC	40MIL
FLASH_SPI_MOSI_IC	40MIL
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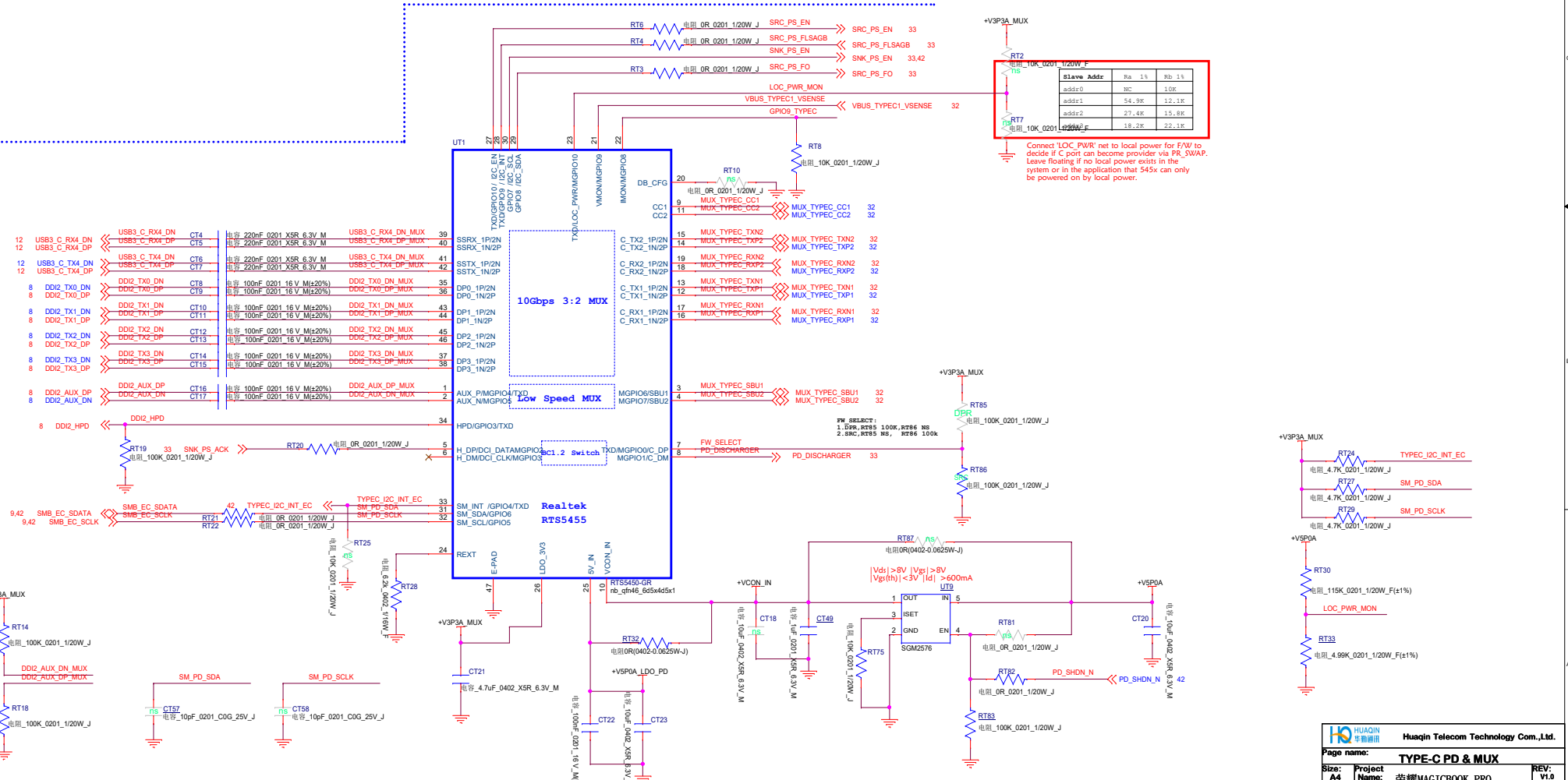


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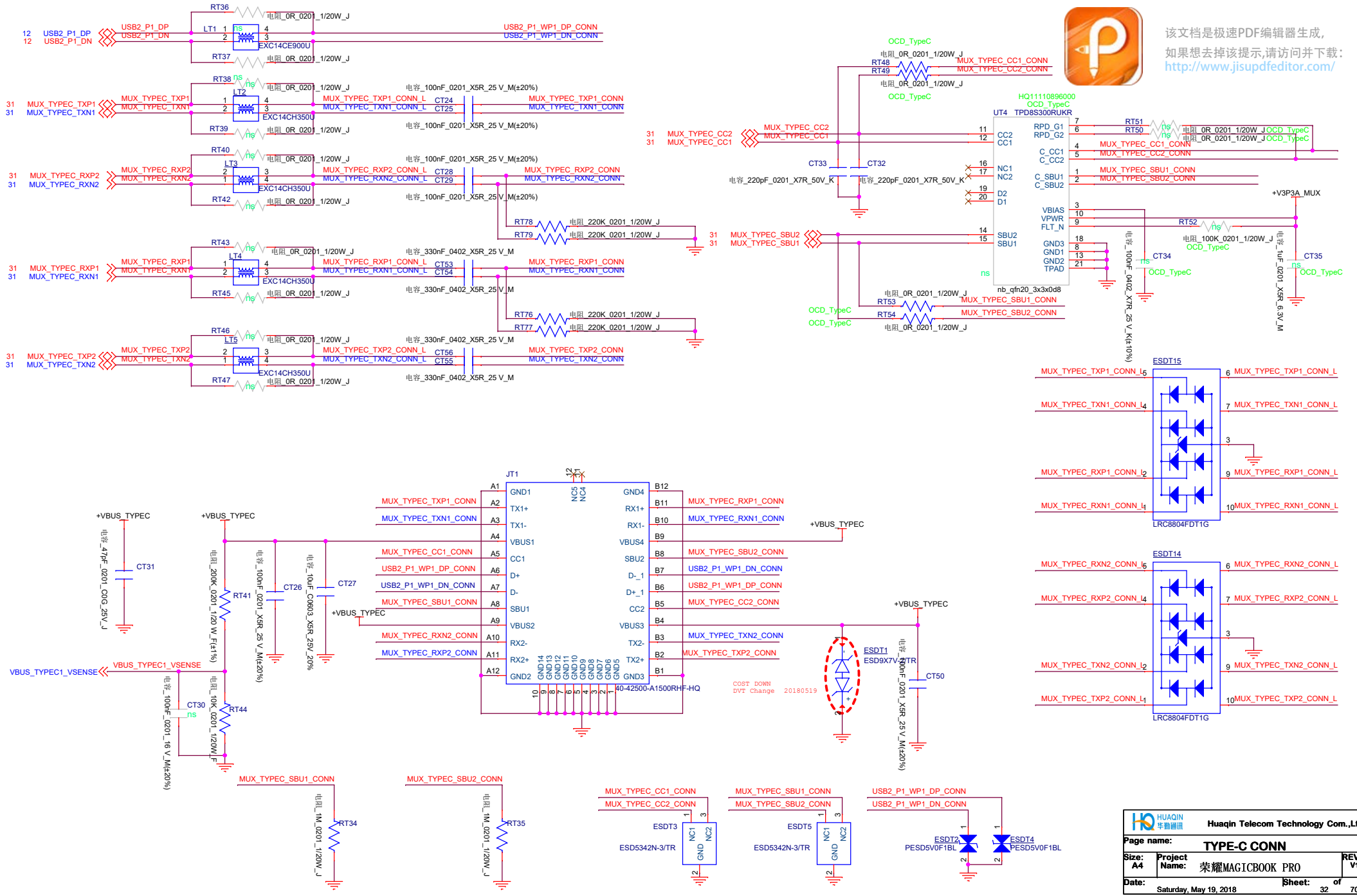
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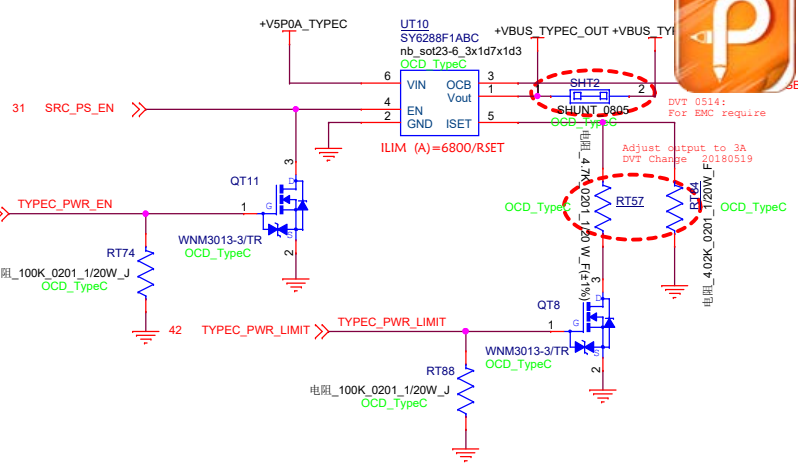
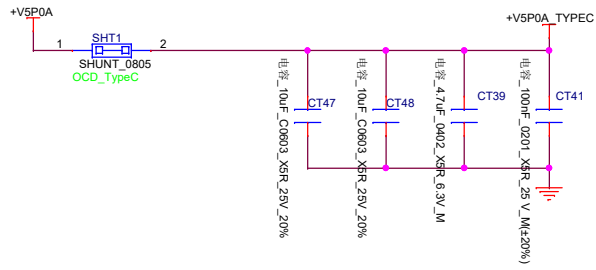


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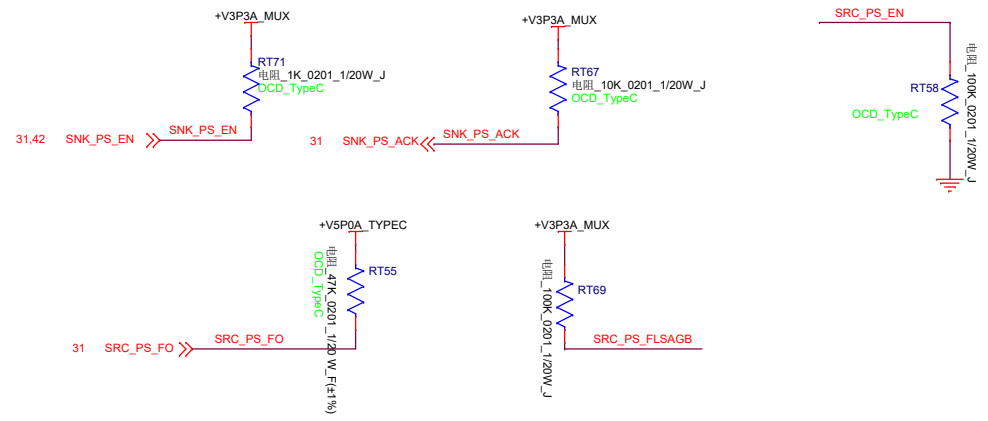
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Date: Saturday, May 19, 2018	Sheet: 32		of 70

TYPE-C OUTPUT

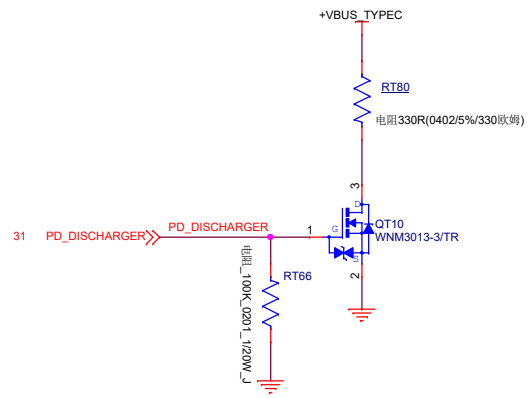


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Discharge



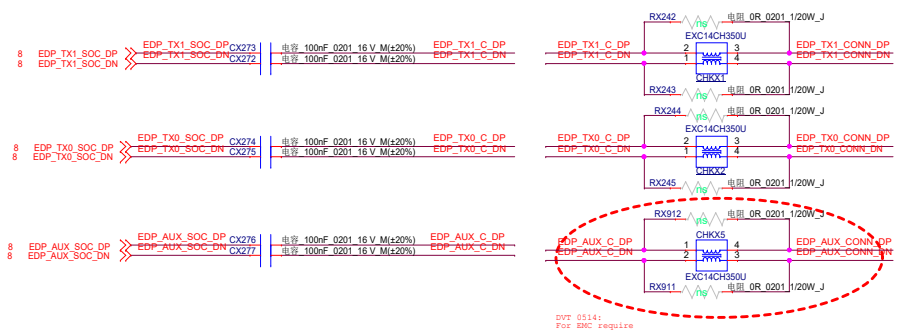
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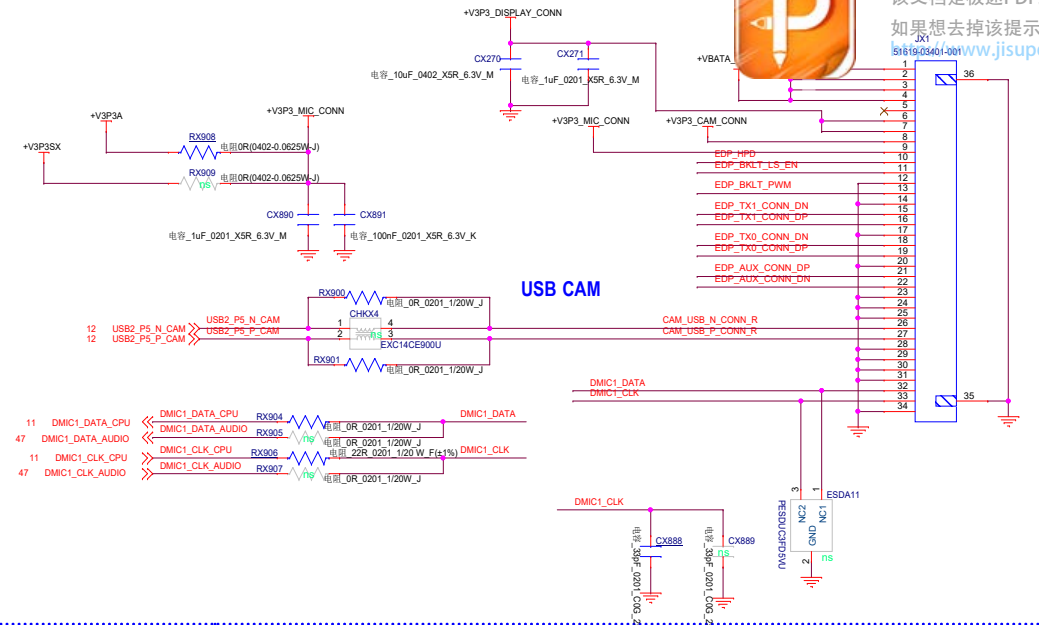
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Date: Saturday, May 19, 2018	Sheet: 34	of 70	

EDP Signal



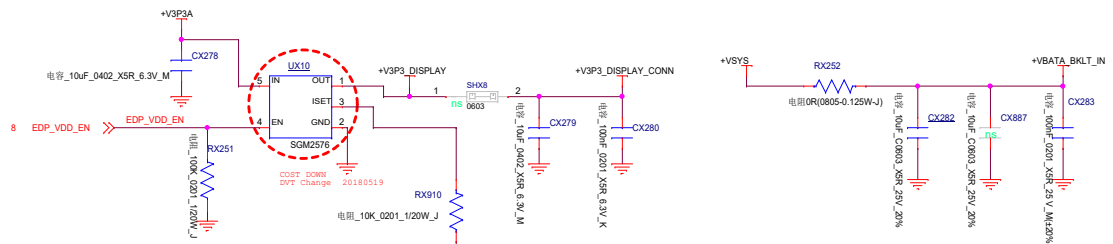
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 For EMC require

EDP & CAM CONN

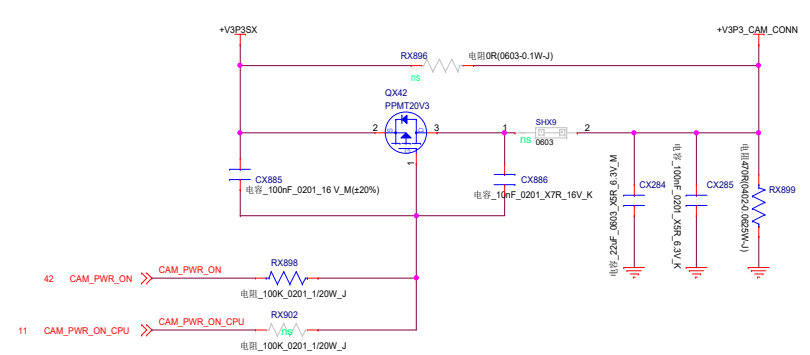


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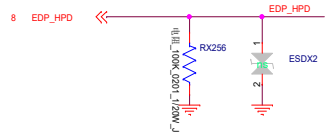
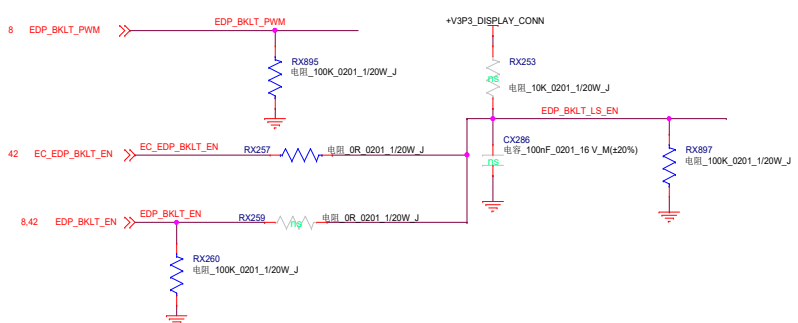
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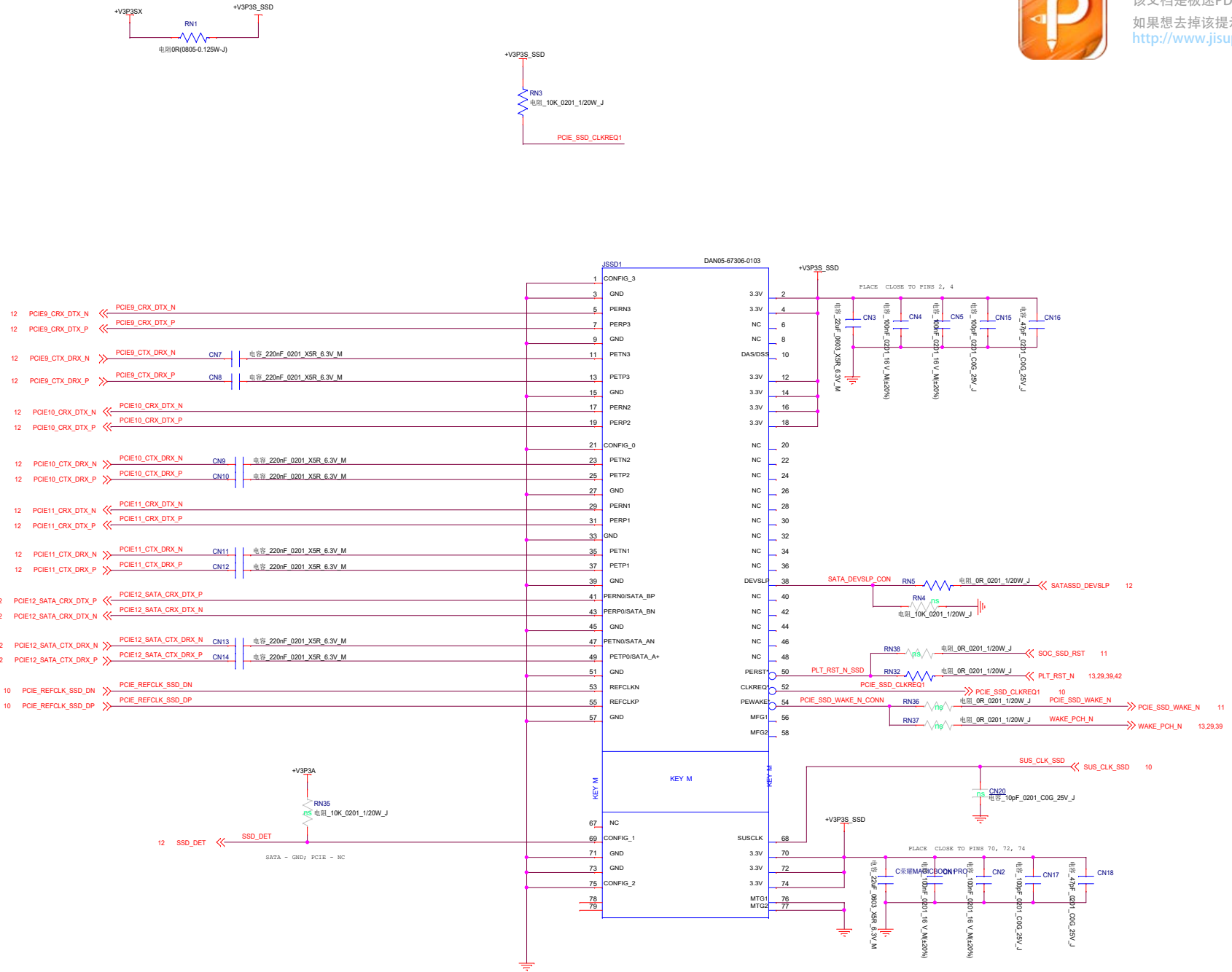


CAM Power

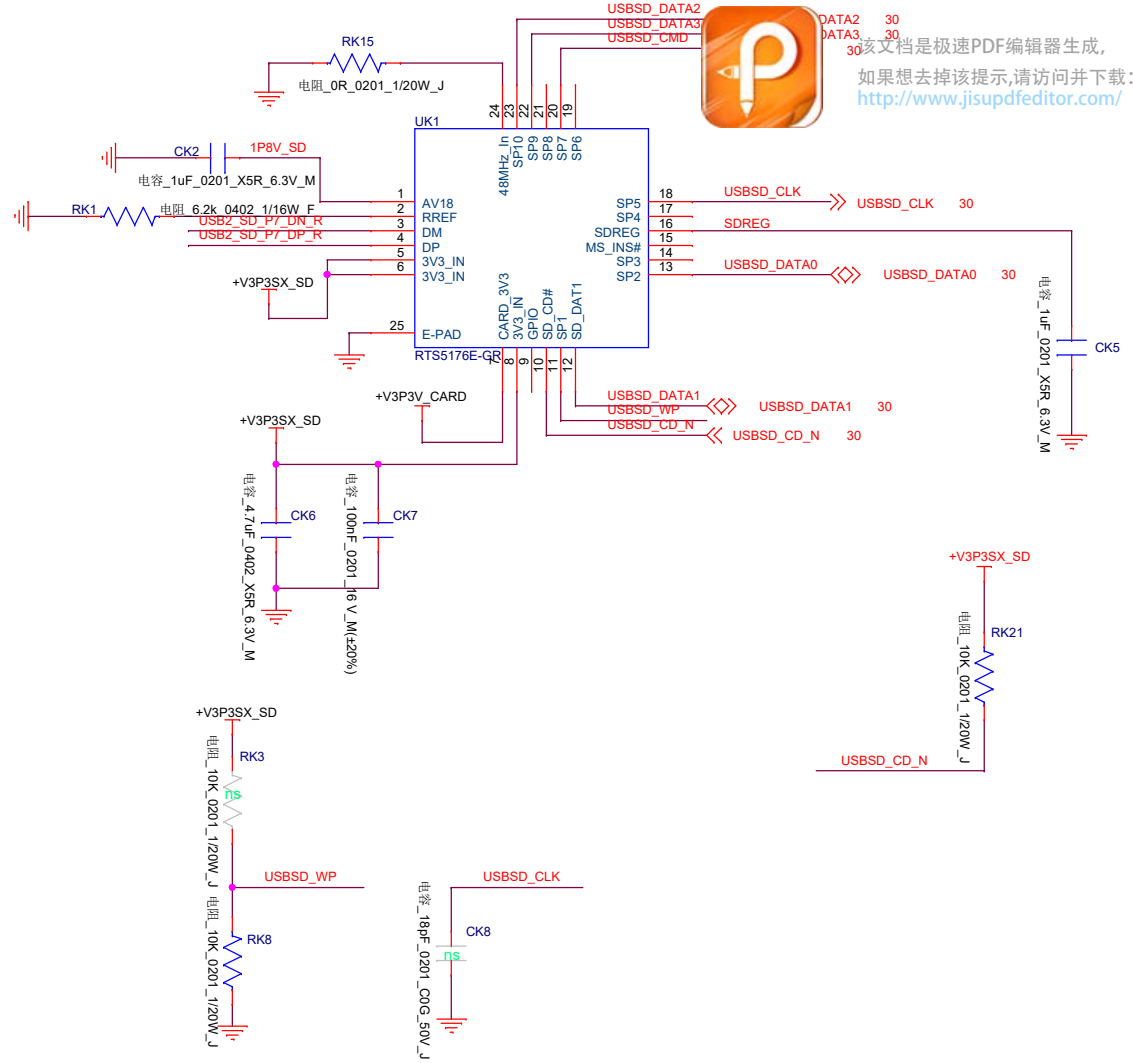
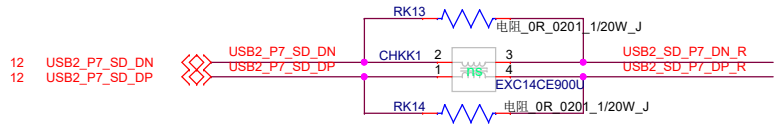
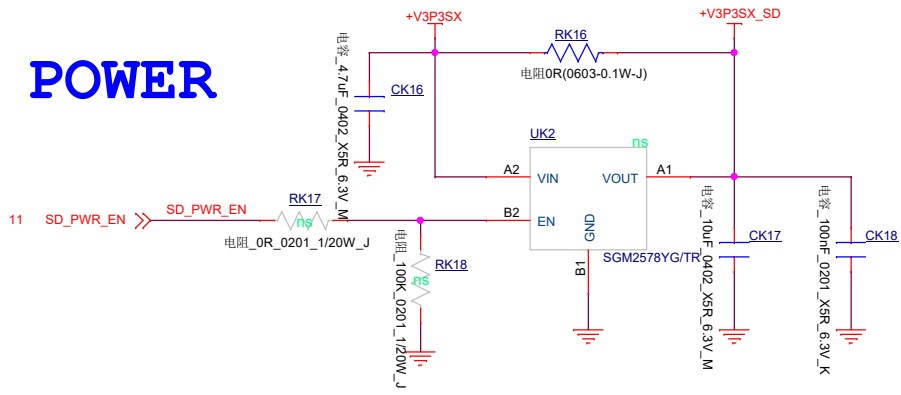


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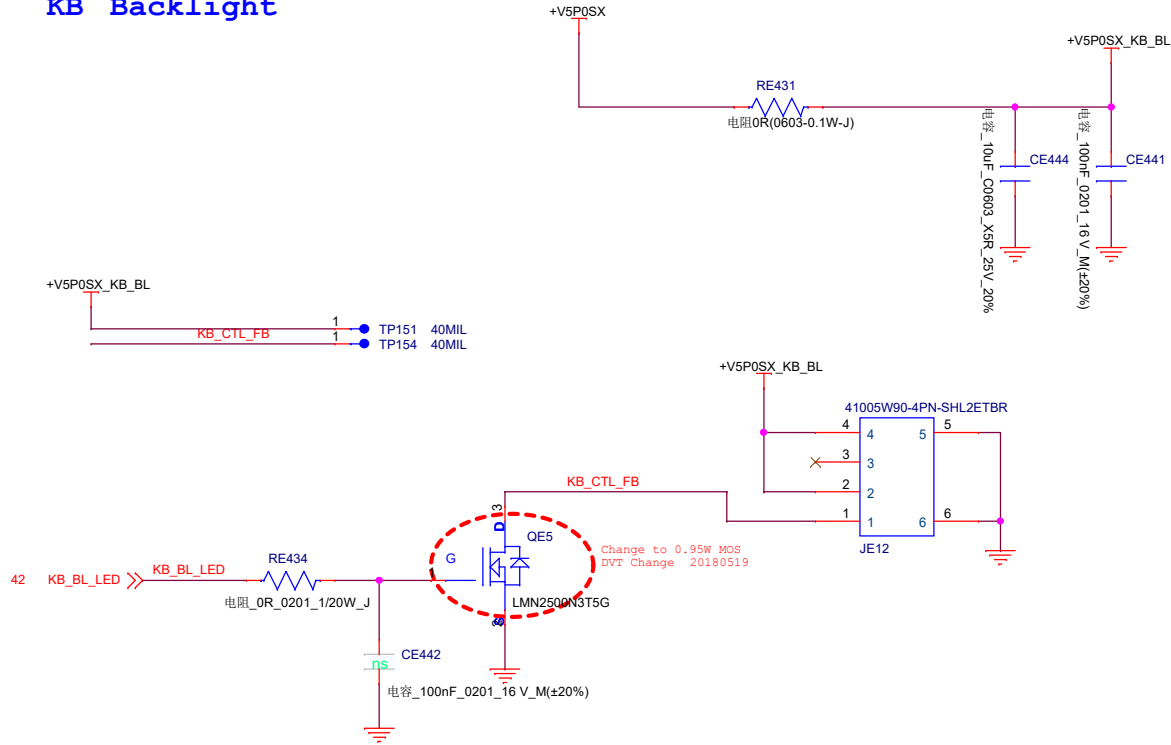
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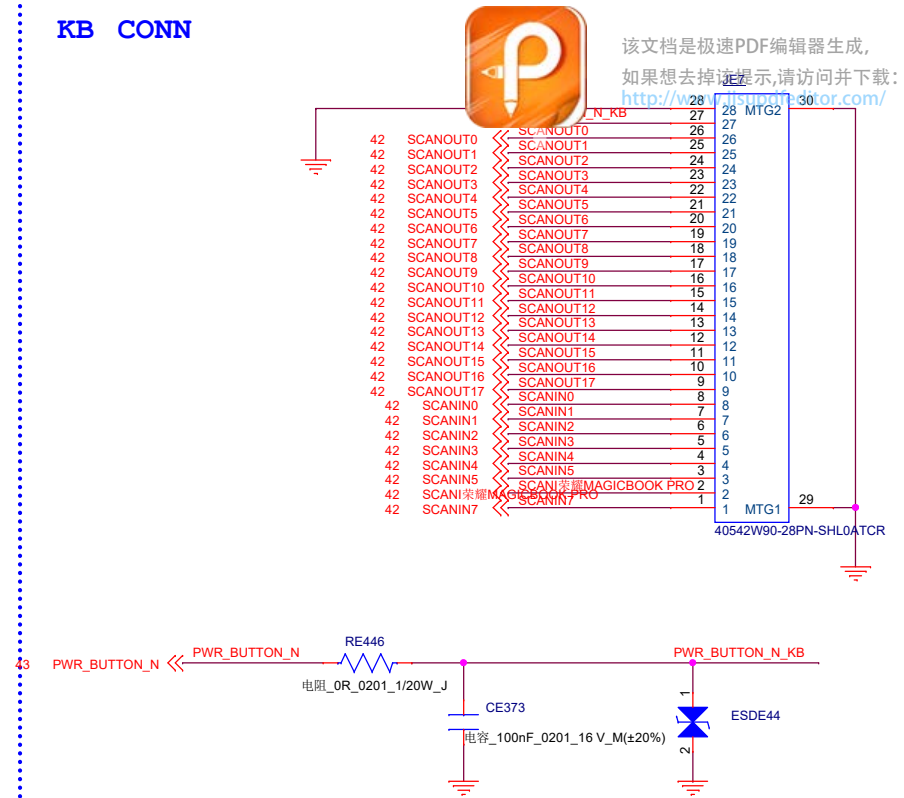
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KB Backlight



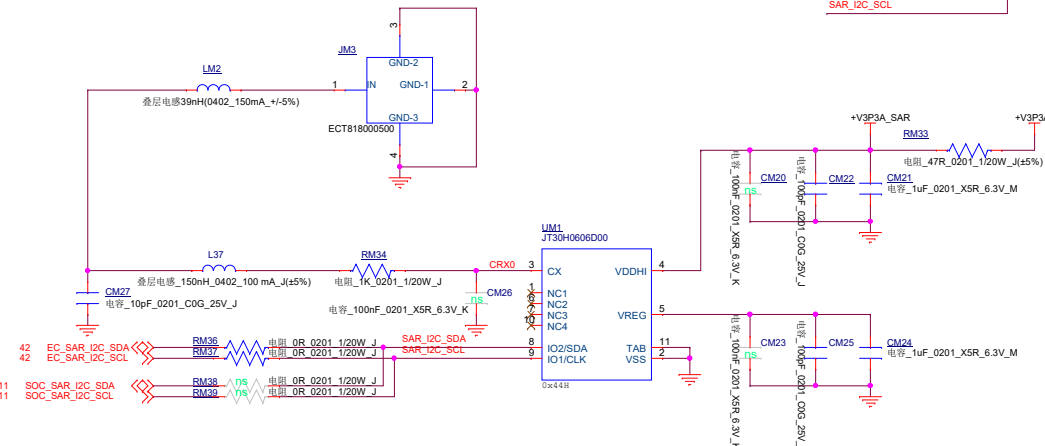
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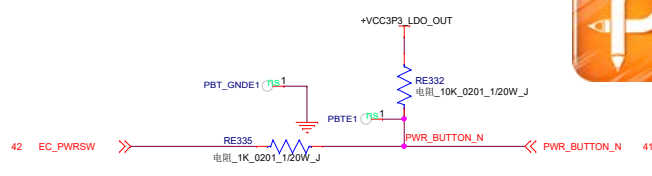
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Page name: KB CON & Backlight			
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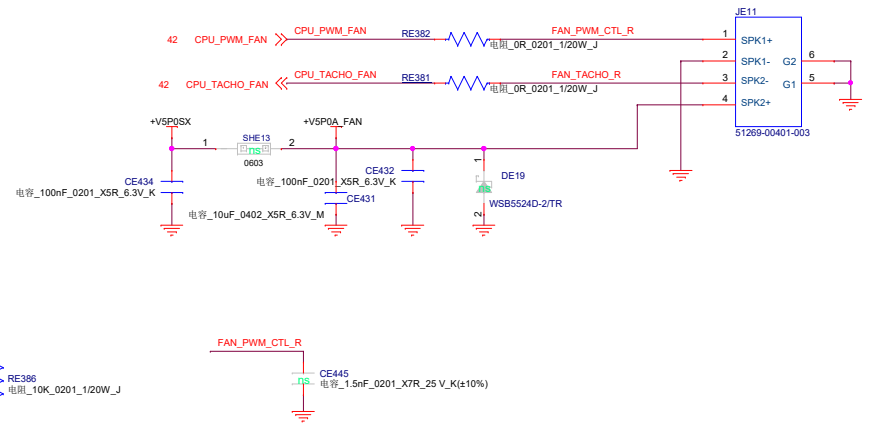


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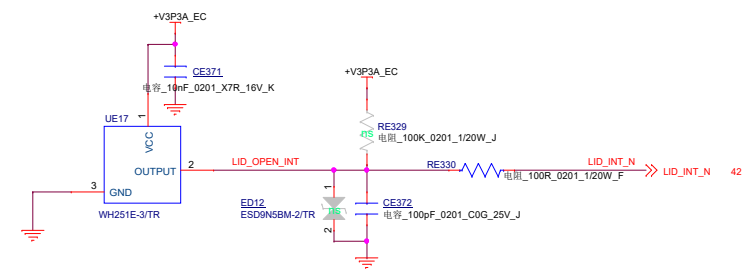


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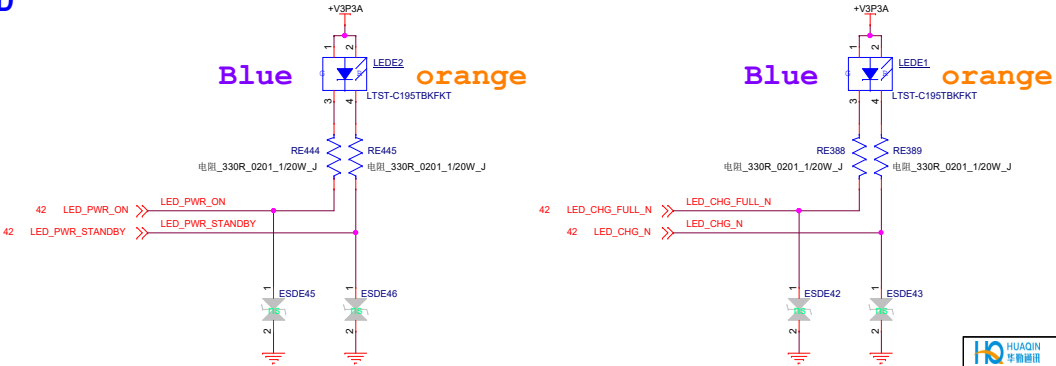
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HALL

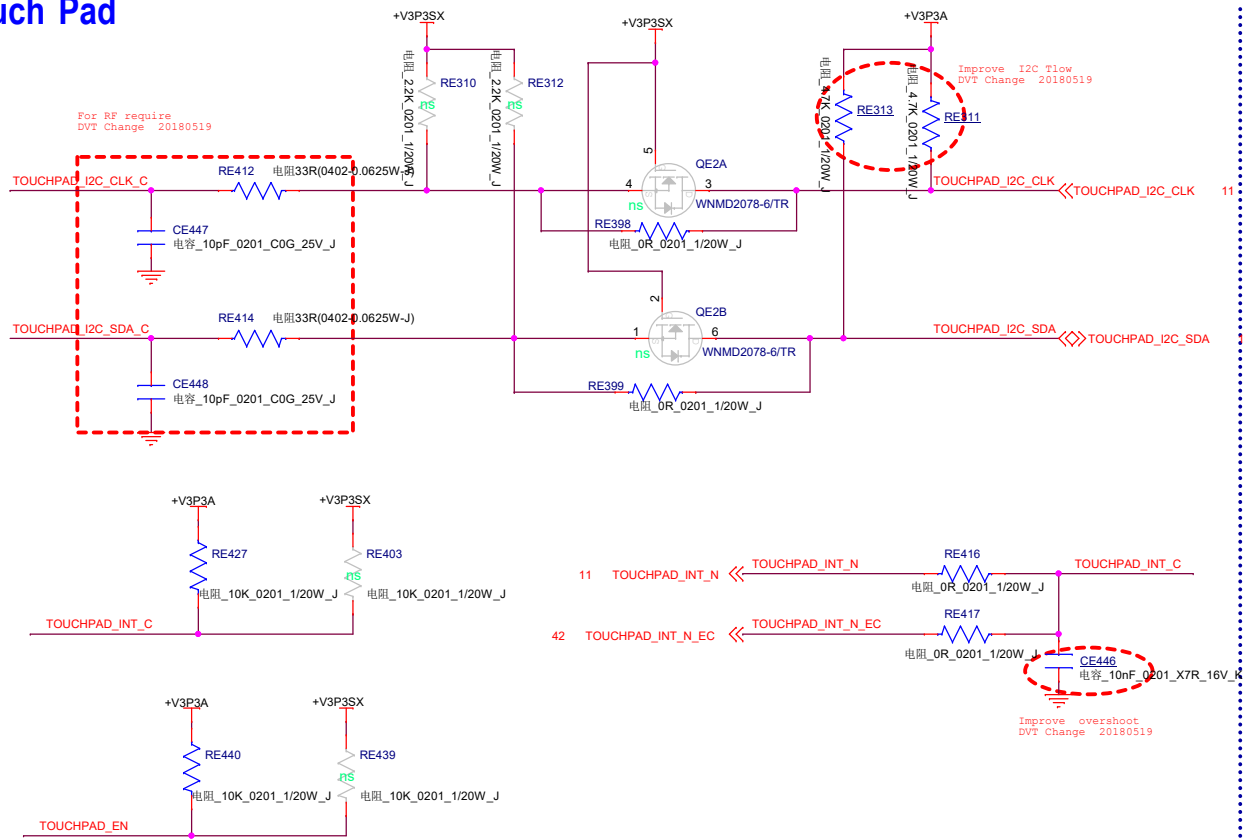


LED

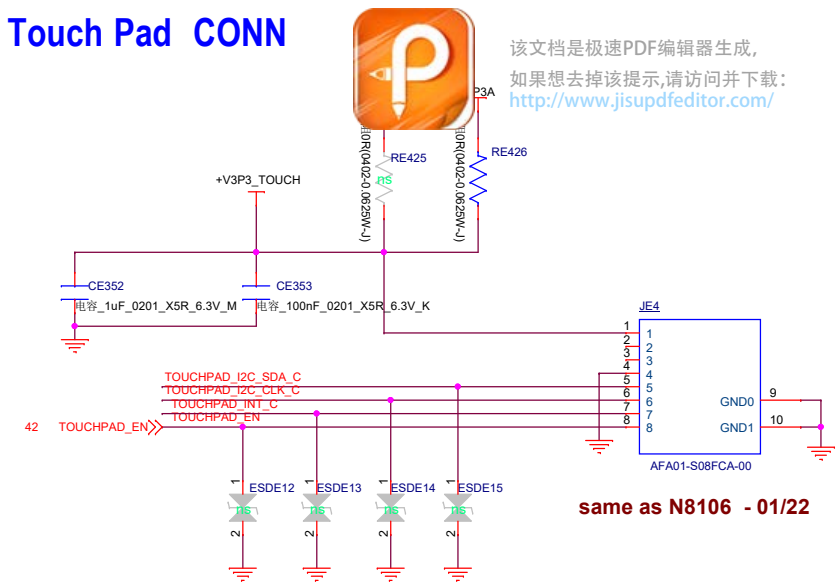


HUAQIN 华勤通讯		FOXCONN	
Page name:	BUTTON/FAN/LED/Hall		
Size: A4	Project Name:	荣耀MAGICBOOK PRO	REV: V1.0
Date:	Thursday, May 24, 2018	Sheet:	43 of 70

Touch Pad



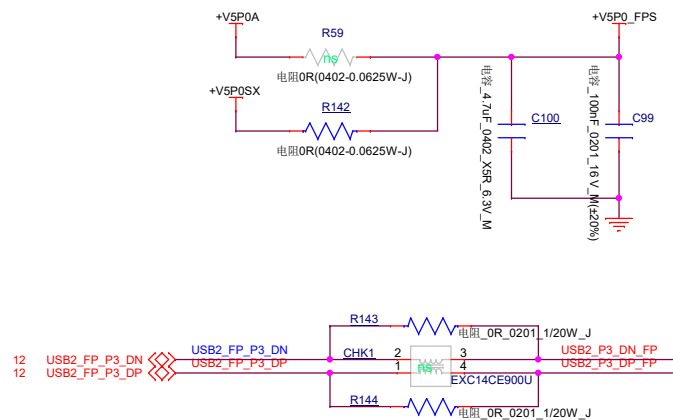
Touch Pad CONN



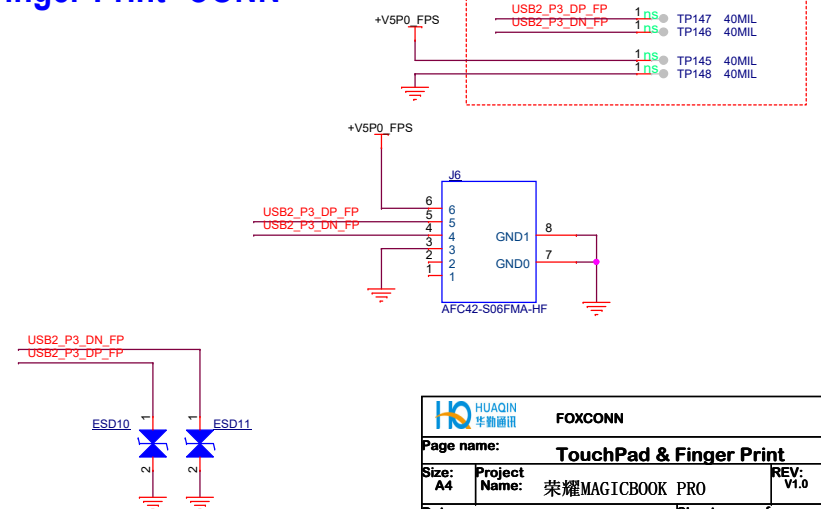
FOR product line

TOUCHPAD_I2C_CLK_C	1ns	TP143	40MIL
TOUCHPAD_I2C_SDA_C	1ns	TP142	40MIL
TOUCHPAD_INT_C	1ns	TP149	40MIL
TOUCHPAD_EN	1ns	TP150	40MIL
	1ns	TP141	40MIL
	1ns	TP144	40MIL

Finger Print



Finger Print CONN

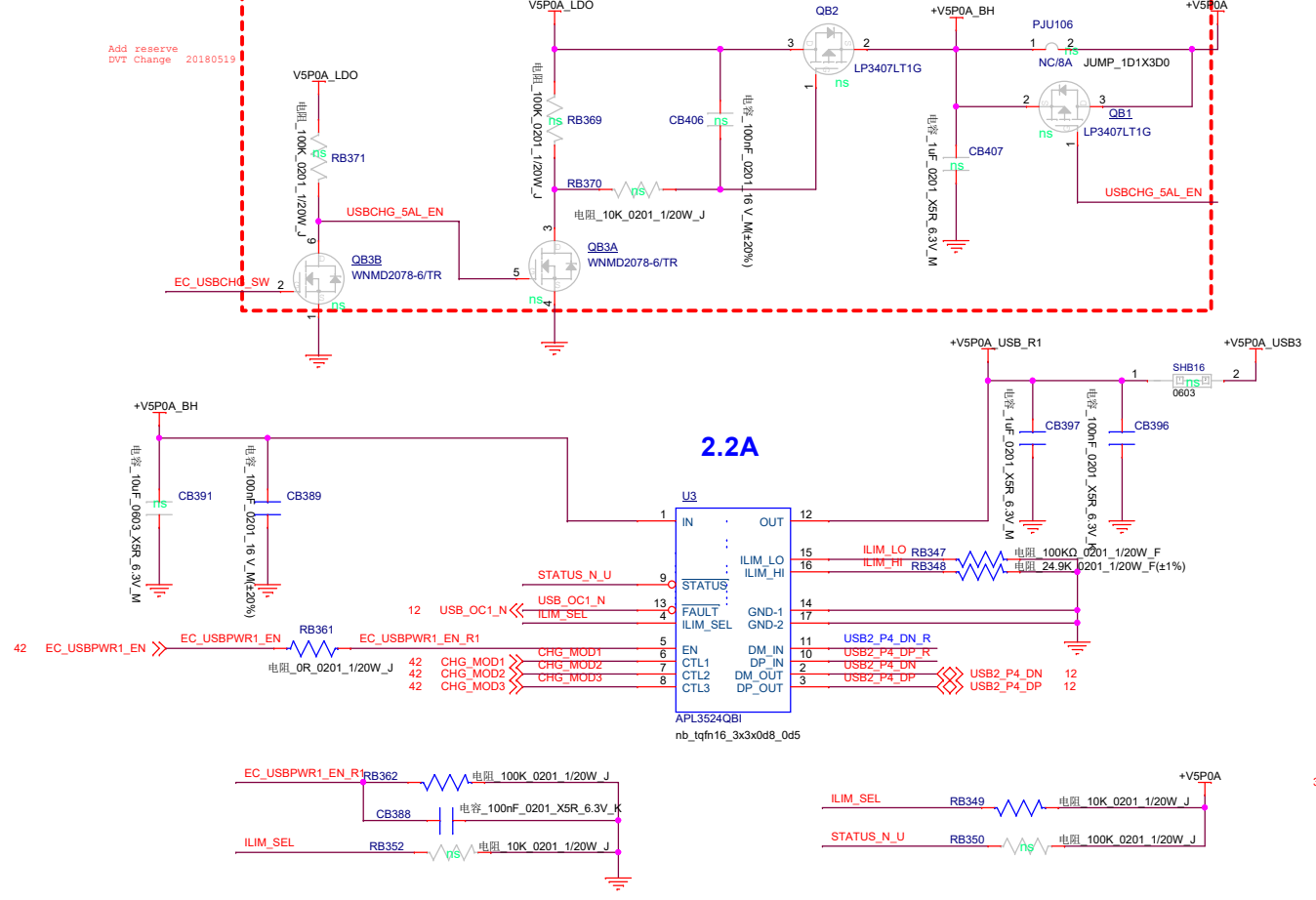


FOR product line

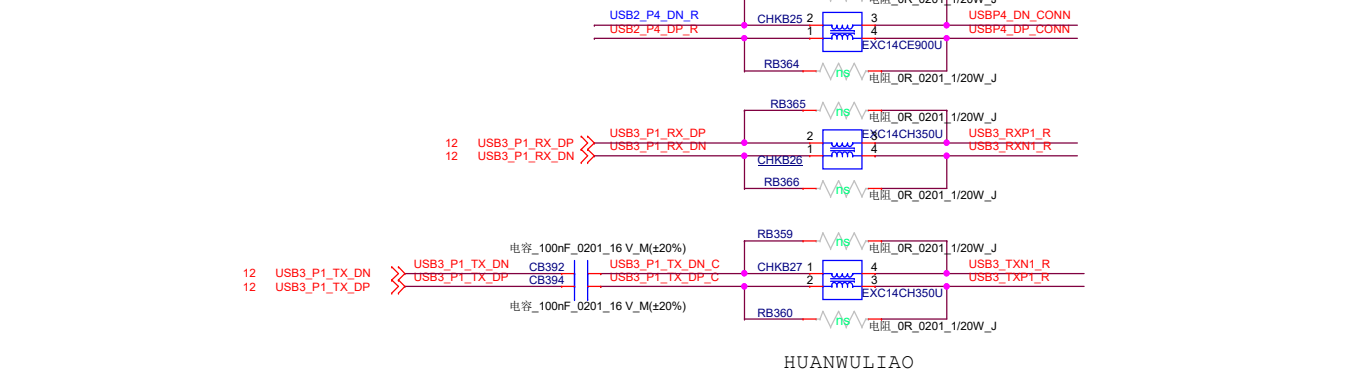
USB2_P3_DP_FP	1ns	TP147	40MIL
USB2_P3_DN_FP	1ns	TP146	40MIL
	1ns	TP145	40MIL
	1ns	TP148	40MIL

Page name: TouchPad & Finger Print			
Size: A4	Project Name: 荣耀MAGICBOOK PRO	REV: V1.0	
Date: Saturday, May 19, 2018	Sheet: 44	of 70	

USB3.1 POWER



USB3.1 Signal

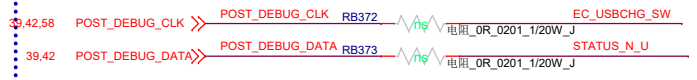
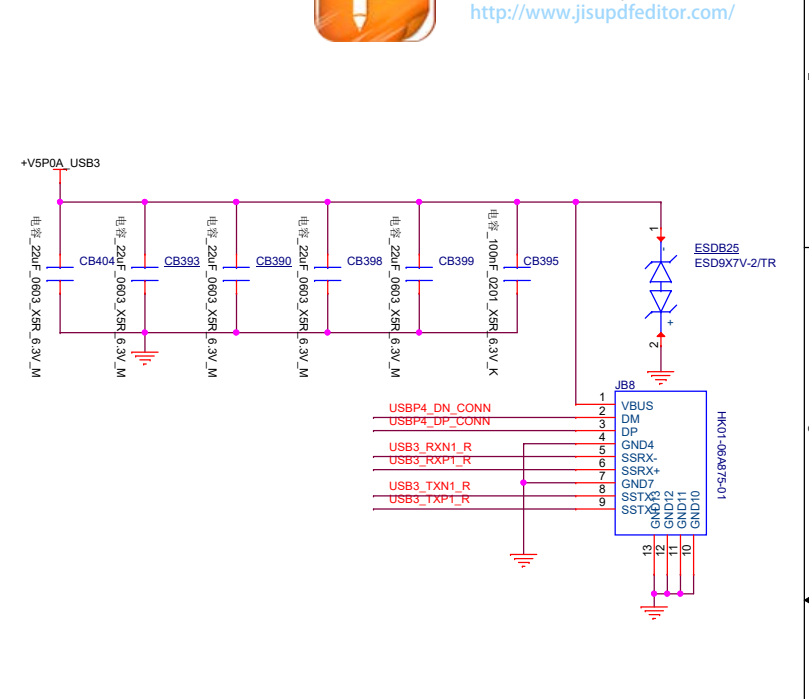


HUANWULIAO

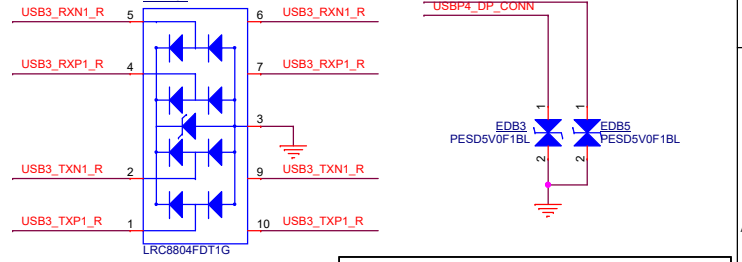


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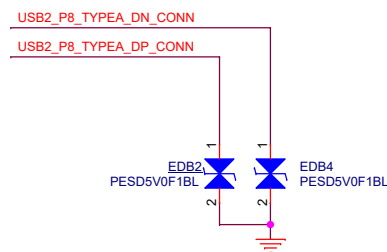
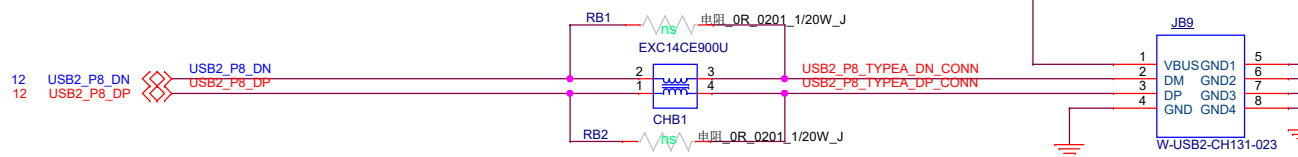
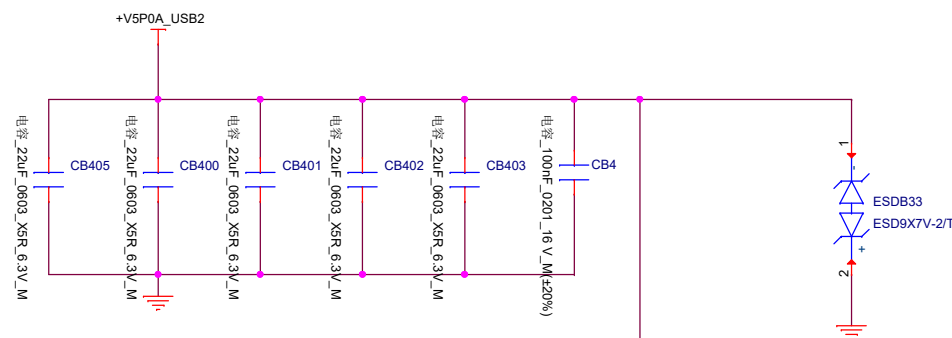
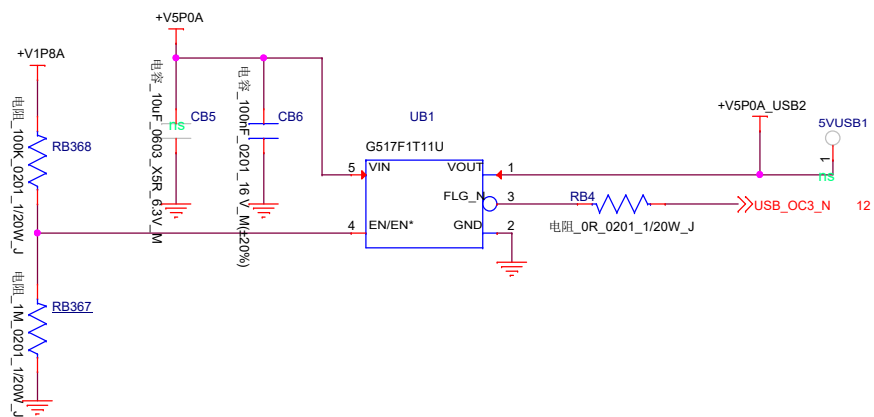
USB3.1 CONN



USB3.1 ESD



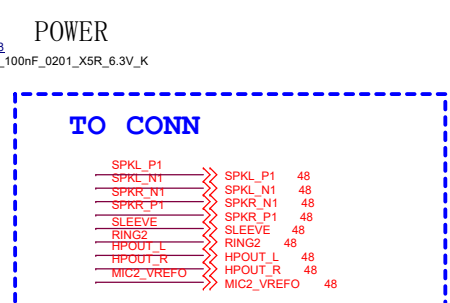
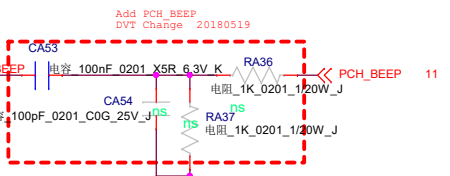
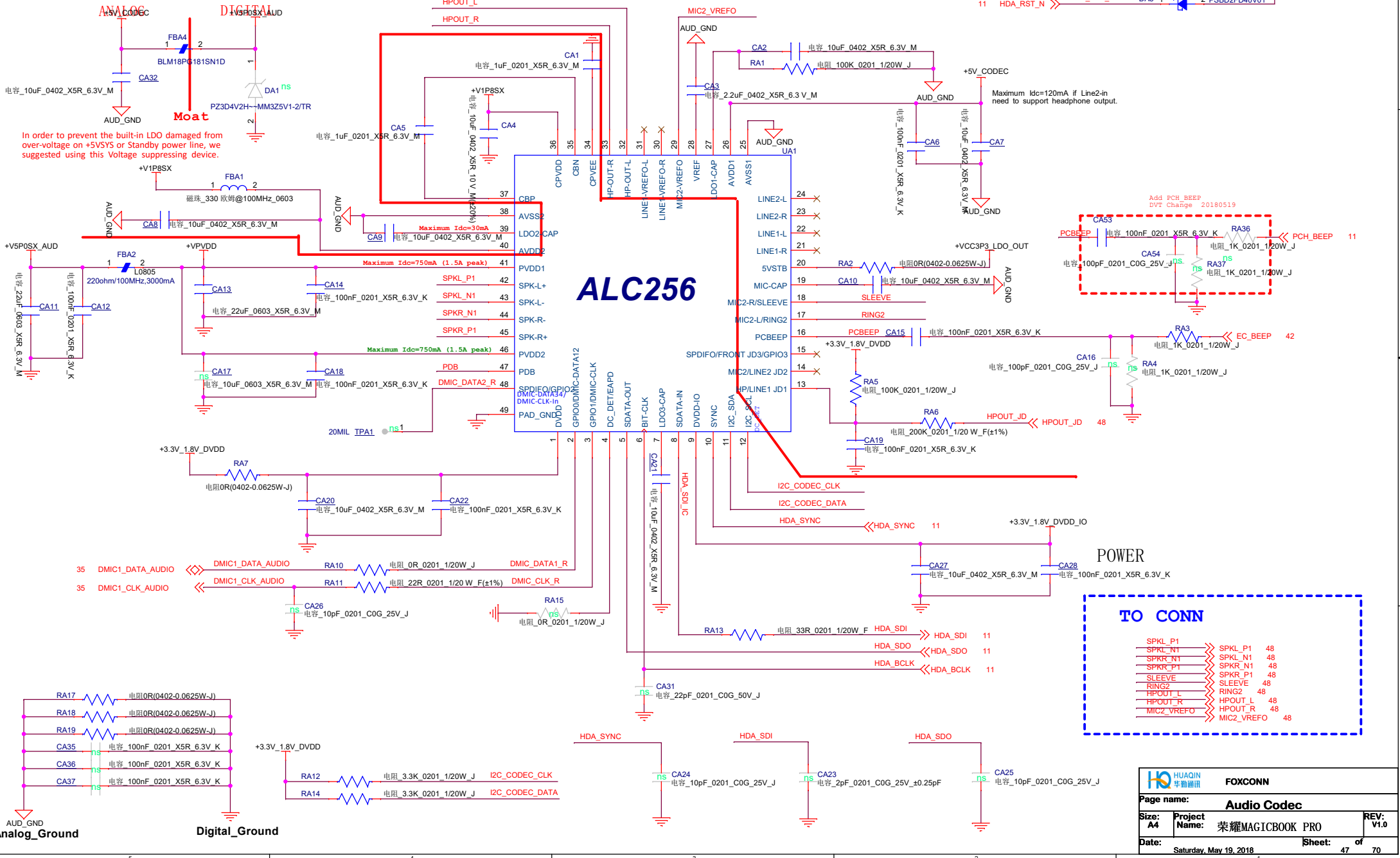
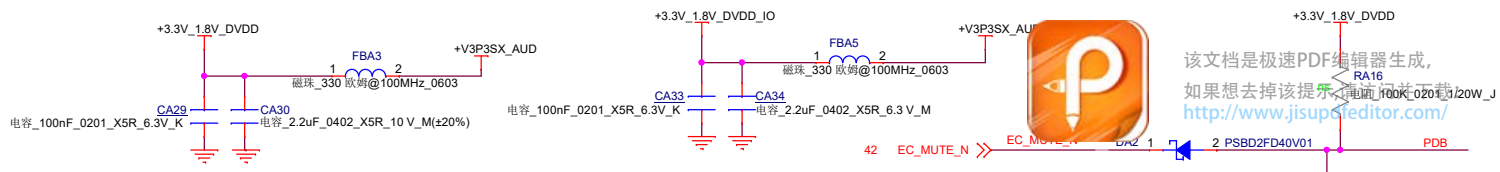
Huaqin Telecom Technology Com.,Ltd.	
Page name: USB3.1	
Size: A4	Project Name: 荣耀MAGICBOOK PRO
Date: Monday, May 21, 2018	REV: V1.0
Sheet: 45	of 70



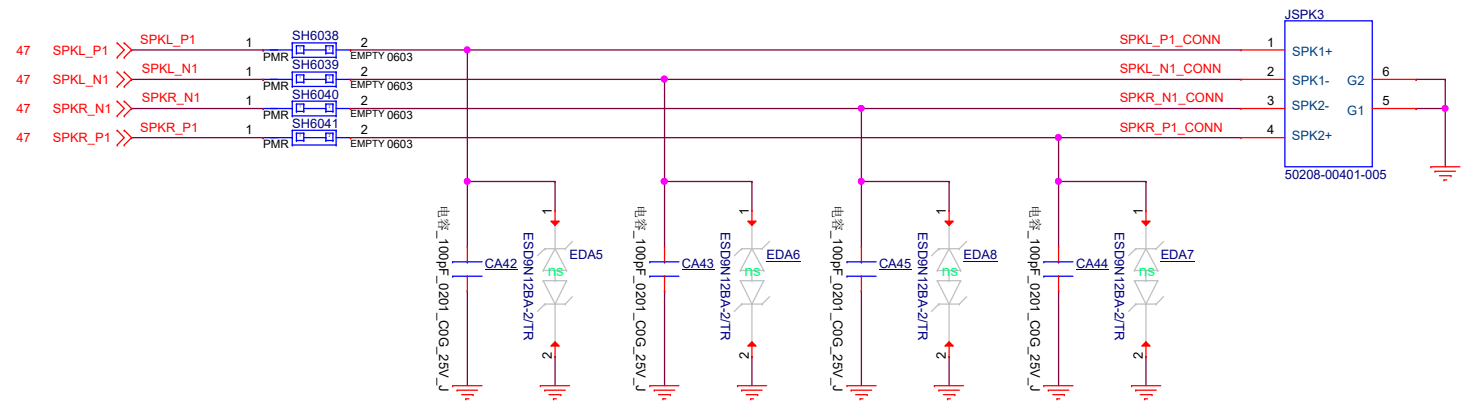
		FOXCONN	
Page name: USB2.0			
Size: A4	Project Name: 荣耀MAGICBOOK PRO	REV: V1.0	
Date: Saturday, May 19, 2018	Sheet: 46	of 70	

Power requirement:
DVDD must >= DVDD_IO

+3.3V 1.8V DVDD	+3.3V 1.8V DVDD IO	Result
3.3V +/-10%	3.3V +/-10%	support
3.3V +/-10%	1.8V +/-5%	support
1.8V +/-5%	1.8V +/-5%	support
1.8V +/-5%	1.5V +/-5%	support
1.8V +/-5%	3.3V +/-10%	Not support



SPEAKER

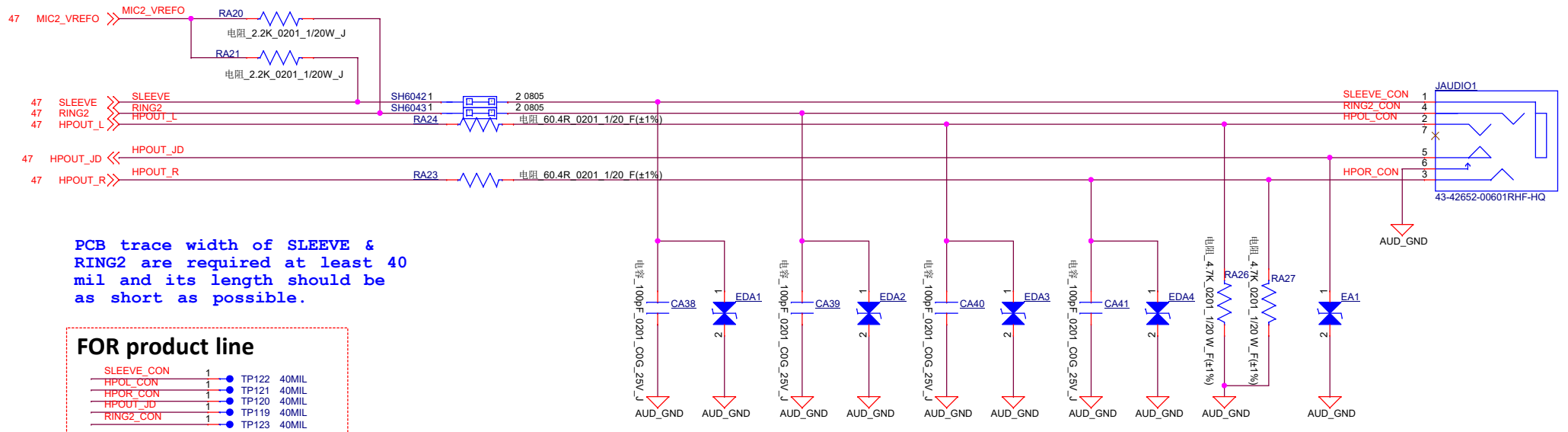


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duct line

SPKR_P1_CONN 1 ● TP124 40MIL
 SPKR_N1_CONN 1 ● TP128 40MIL

Combo Jack

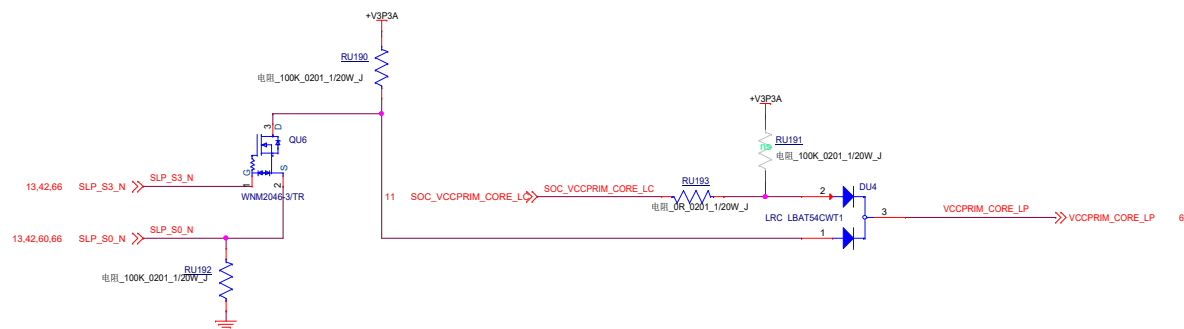


PCB trace width of SLEEVE & RING2 are required at least 40 mil and its length should be as short as possible.

FOR product line


SLEEVE_CON 1 ● TP122 40MIL
 HPOL_CON 1 ● TP121 40MIL
 HPOR_CON 1 ● TP120 40MIL
 HPOUT_JD 1 ● TP119 40MIL
 RING2_CON 1 ● TP123 40MIL

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Page name: SKP & Audio Jack			
Size: A4	Project Name: 荣耀MAGICBOOK PRO	REV: V1.0	
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Size: A4	Project Name: 荣耀MAGICBOOK PRO	REV: V1.0	
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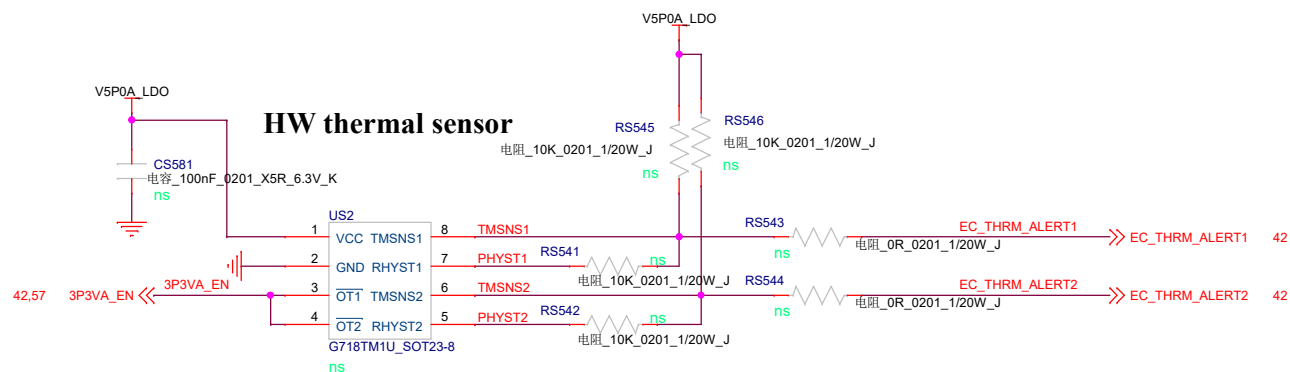


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Date: Saturday, May 19, 2018	Sheet: 53 of 70		




over temperature threshold:
 $RSET=3*RTMH$
 $92+/-30C$
 Hysteresis temperature threshold.
 $RHYST=(RSET*RTML)/(3*RTML-RSET)$
 $56+/-30C$

HUAQIN 华勤通讯		FOXCONN	
Page name: Thermal sensor			
Size: A4	Project Name: 荣耀MAGICBOOK PRO	REV: V1.0	
Date: Saturday, May 19, 2018	Sheet: 54	of 70	

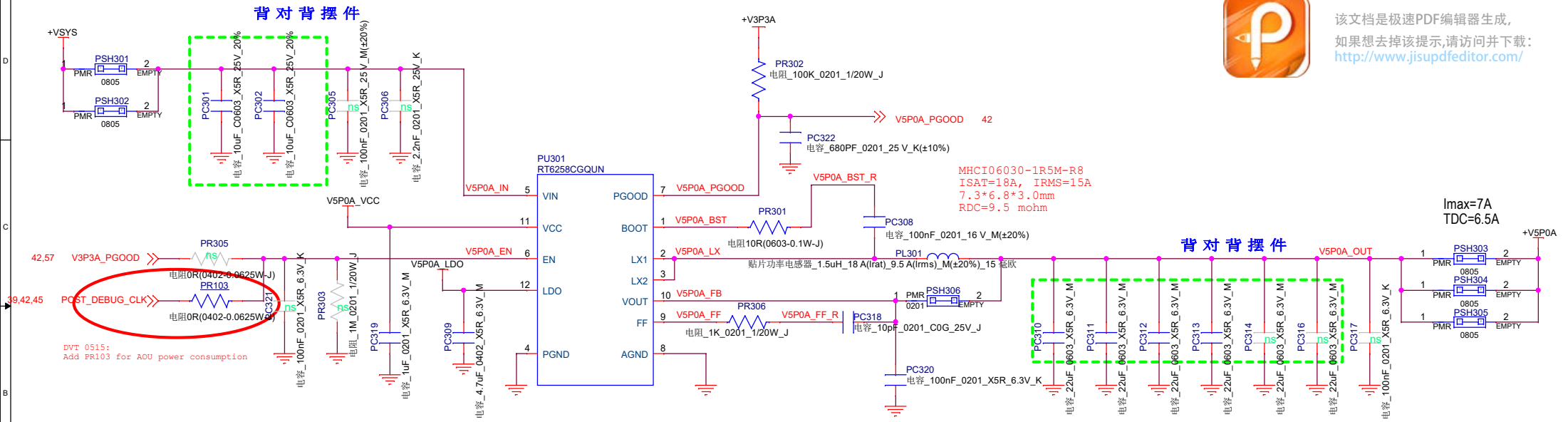


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Date: Saturday, May 19, 2018	Sheet:	55	of 70

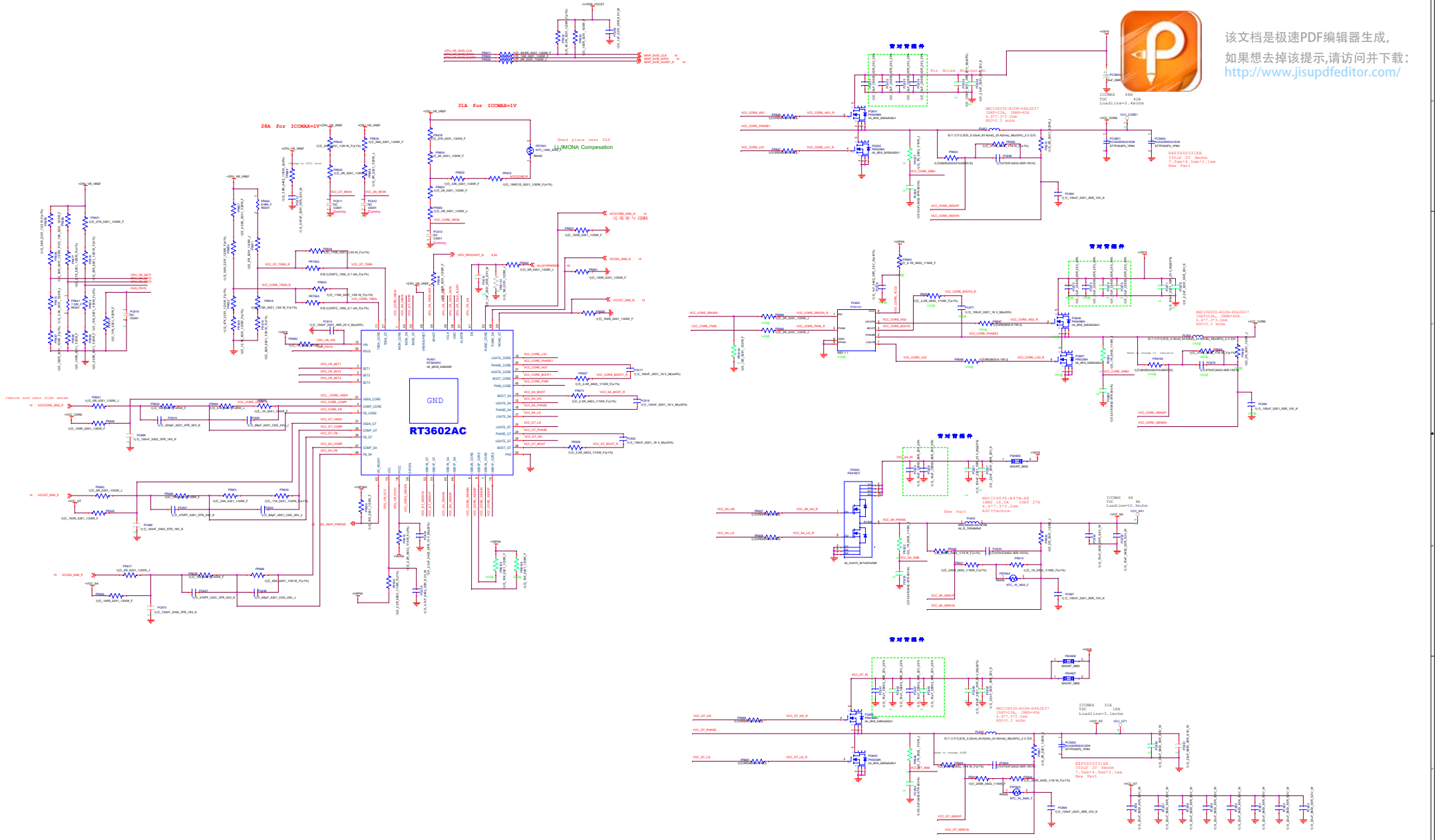


背对背摆件



背对背摆件

HUAQIN 华勤通讯		FOXCONN	
Page name: POWER DELIVERY 5V			
Size: A4	Project Name: 荣耀MAGICBOOK PRO	REV: V1.0	
Date: Wednesday, May 23, 2018	Sheet: 58	of 70	






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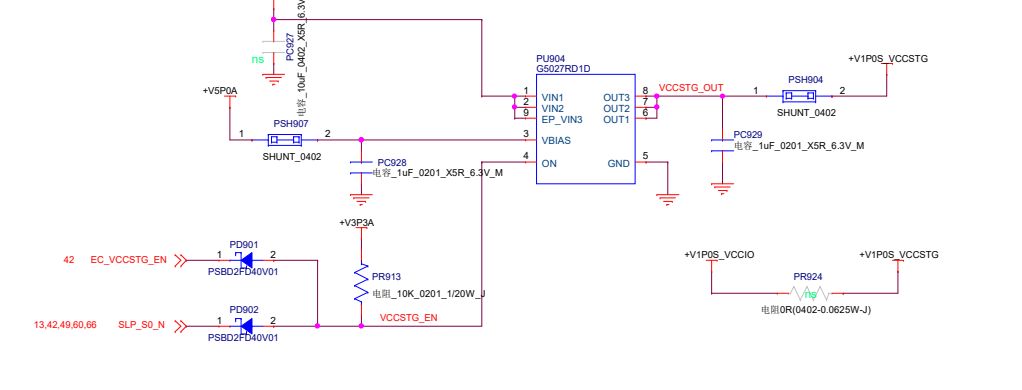
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Page name: POWER DELIVERY Reserve2			
Size: A4	Project Name: 荣耀MAGICBOOK PRO	REV: V1.0	
Date: Saturday, May 19, 2018	Sheet: 64	of	70



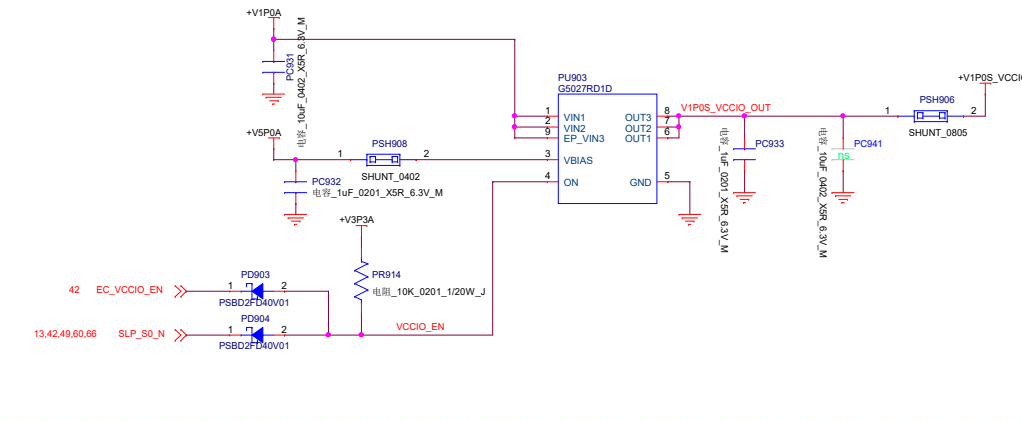
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Page name: POWER DELIVERY Reserve3			
Size: A4	Project Name: 荣耀MAGICBOOK PRO	REV: V1.0	
Date: Saturday, May 19, 2018	Sheet: 65	of	70

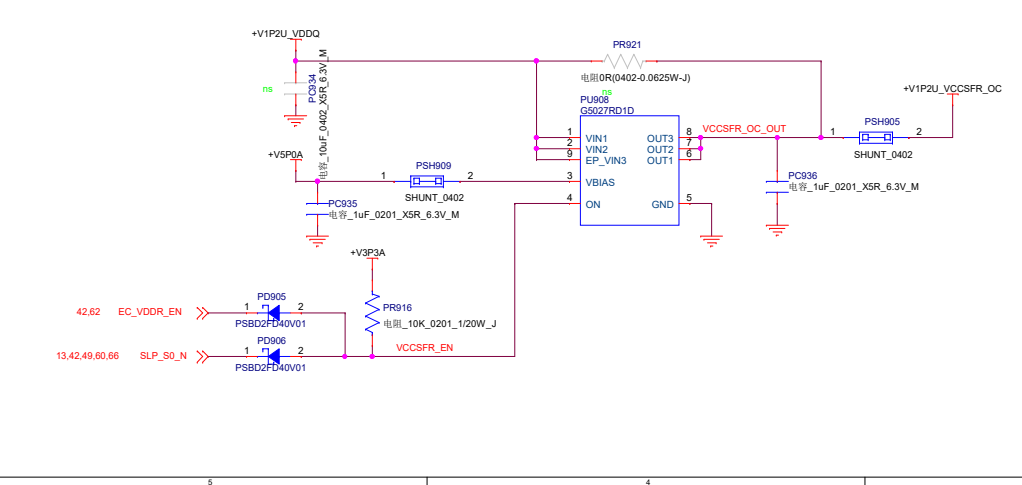
+VCCSTG



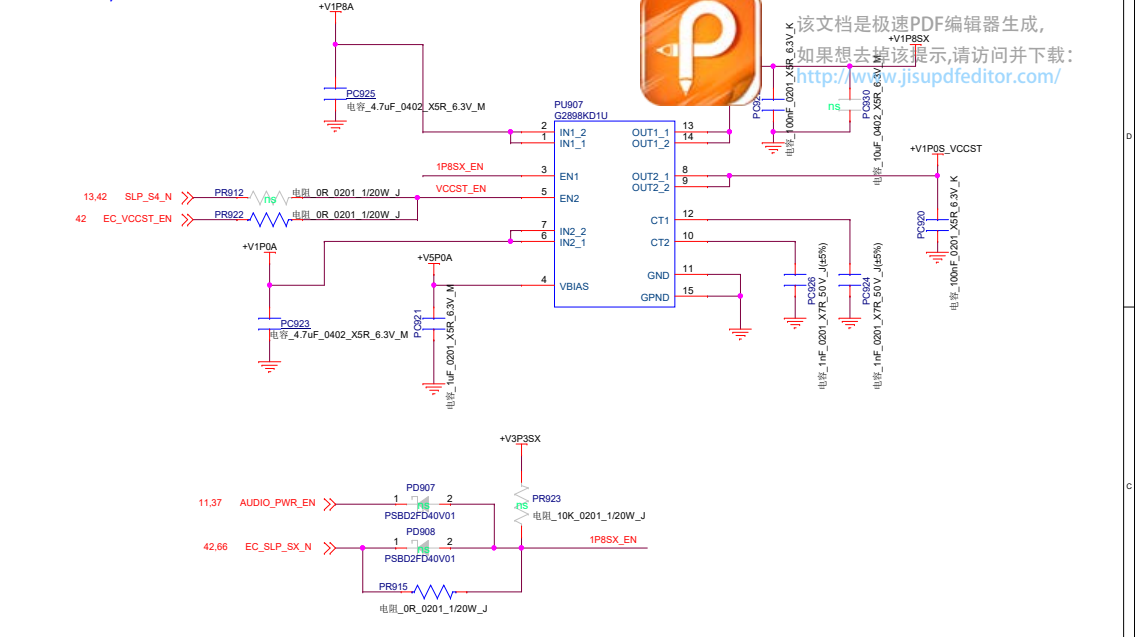
+VCCIO



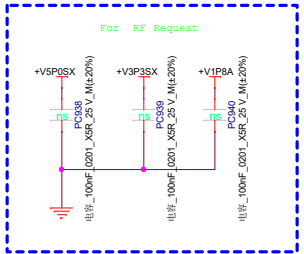
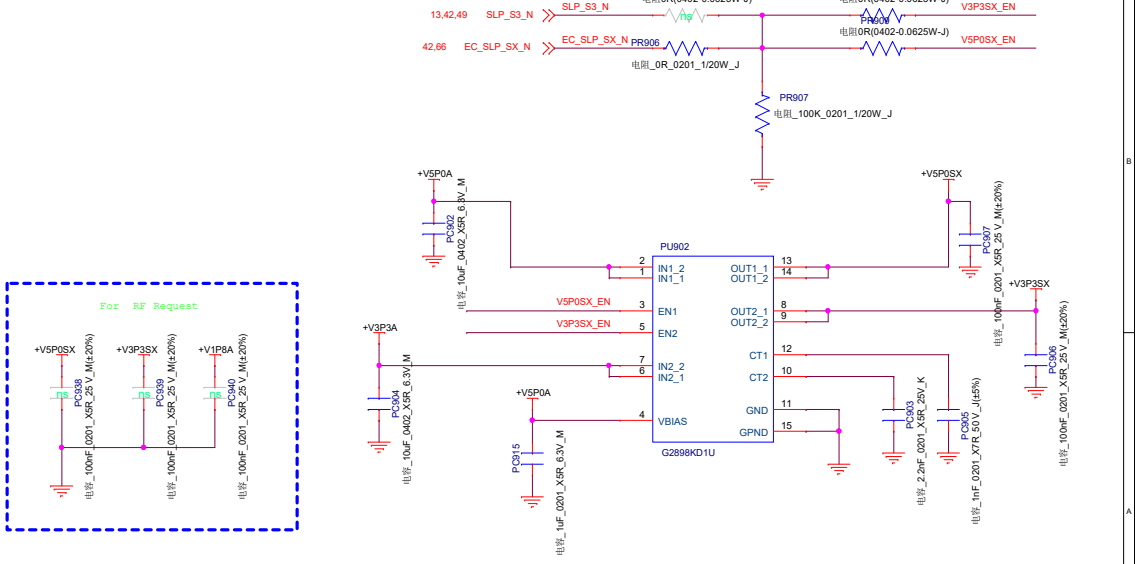
+V1P2U_VCCSFR_OC



+V1P8SX, +VCCST




+V3P3SX, +V5P0SX



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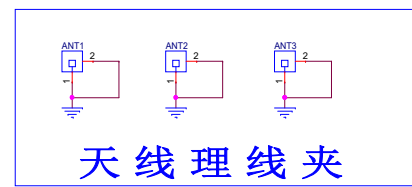
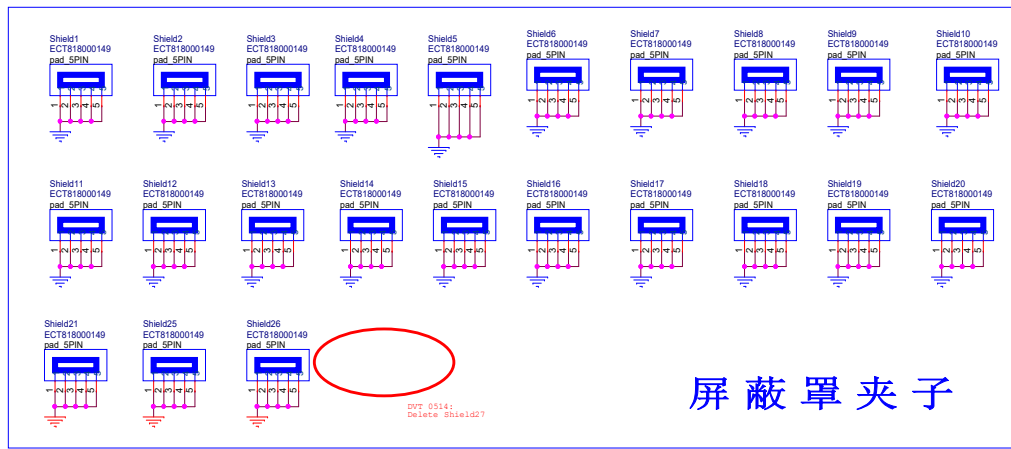
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Size: A4	Project Name: 荣耀MAGICBOOK PRO	REV: V1.0	
Date: Saturday, May 19, 2018		Sheet: 68	of 70

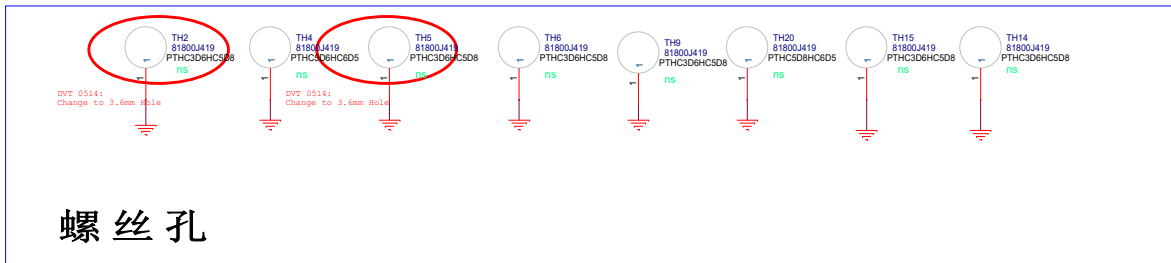


天线理线夹

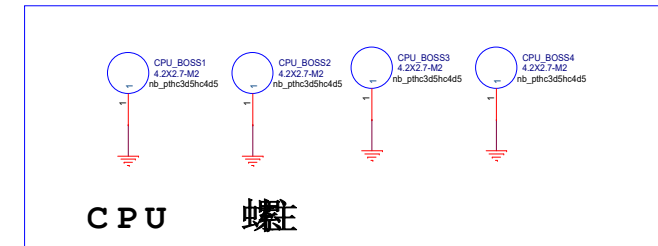


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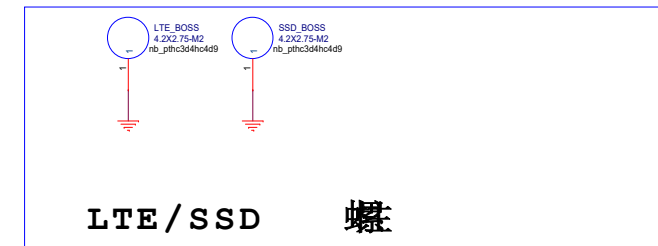
屏蔽罩夹子



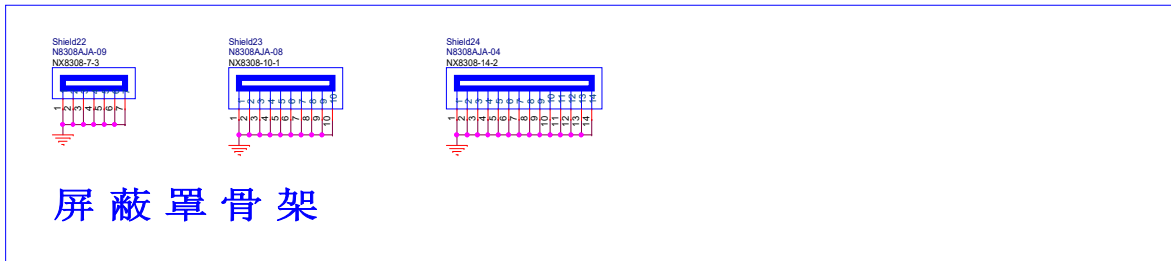
螺丝孔



CPU 螺



LTE/SSD 螺



屏蔽罩骨架



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D

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Size: A4	Project Name: 荣耀MAGICBOOK PRO	REV: V1.0	
Date: Saturday, May 19, 2018		Sheet: 70	of 70

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